

PJA3424E

30V N-Channel Enhancement Mode MOSFET– ESD Protected

Voltage

30 V

Current

4.2A

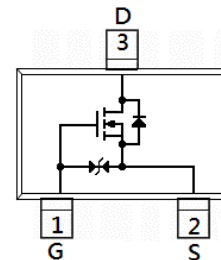
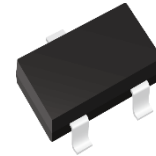
Features

- $R_{DS(ON)}$, $V_{GS}@10V$, $I_D@4.2A < 42m\Omega$
- $R_{DS(ON)}$, $V_{GS}@4.5V$, $I_D@3.5A < 48m\Omega$
- $R_{DS(ON)}$, $V_{GS}@2.5V$, $I_D@2.8A < 55m\Omega$
- Advanced Trench Process Technology
- ESD Protected
- Specially Designed for Switch Load, PWM Application, etc
- Lead free in compliance with EU RoHS 2.0
- Green molding compound as per IEC 61249 Standard

Mechanical Data

- Case : SOT-23 Package
- Terminals : Solderable per MIL-STD-750, Method 2026
- Approx. Weight : 0.0003 ounces, 0.0084 grams

SOT-23



Maximum Ratings and Thermal Characteristics ($T_A=25^\circ C$ unless otherwise noted)

PARAMETER	SYMBOL	LIMIT	UNITS
Drain-Source Voltage	V_{DS}	30	V
Gate-Source Voltage	V_{GS}	± 12	V
Continuous Drain Current	I_D	4.2	A
Pulsed Drain Current	I_{DM}	16.8	A
Power Dissipation	P_D	$T_a=25^\circ C$	1.25
		Derate above $25^\circ C$	10
Operating Junction and Storage Temperature Range	T_J, T_{STG}	-55~150	$^\circ C$
Typical Thermal Resistance	$R_{\theta JA}$	100	$^\circ C/W$
- Junction to Ambient ^(Note 3)			

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Electrical Characteristics (T_A=25°C unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V, I _D =250uA	30	-	-	V
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _D =250uA	0.5	0.8	1.3	V
Drain-Source On-State Resistance	R _{DS(on)}	V _{GS} =10V, I _D =4.2A	-	32	42	mΩ
		V _{GS} =4.5V, I _D =3.5A	-	35	48	
		V _{GS} =2.5V, I _D =2.8A	-	44	55	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =30V, V _{GS} =0V	-	-	1	uA
Gate-Source Leakage Current	I _{GSS}	V _{GS} =±10V, V _{DS} =0V	-	-	±10	uA
Dynamic (Note 5)						
Total Gate Charge	Q _g	V _{DS} =15V, I _D =4.2A, V _{GS} =4.5V(Note 1,2)	-	6	-	nC
Gate-Source Charge	Q _{gs}		-	1	-	
Gate-Drain Charge	Q _{gd}		-	1.5	-	
Input Capacitance	C _{iss}	V _{DS} =15V, V _{GS} =0V, f=1.0MHZ	-	430	-	pF
Output Capacitance	C _{oss}		-	45	-	
Reverse Transfer Capacitance	C _{rss}		-	40	-	
Turn-On Delay Time	t _{d(on)}	V _{DD} =15V, I _D =1A, V _{GS} =10V, R _G =3Ω(Note 1,2)	-	3	-	ns
Turn-On Rise Time	t _r		-	25	-	
Turn-Off Delay Time	t _{d(off)}		-	26	-	
Turn-Off Fall Time	t _f		-	18	-	
Drain-Source Diode						
Maximum Continuous Drain-Source Diode Forward Current	I _s	---	-	-	1.5	A
Diode Forward Voltage	V _{SD}	I _s =1.0A, V _{GS} =0V	-	0.77	1.2	V

NOTES :

1. Pulse width ≤ 300us, Duty cycle ≤ 2%.
2. Essentially independent of operating temperature typical characteristics.
3. R_{θJA} is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins mounted on a 1 inch FR-4 with 2oz. square pad of copper.
4. The maximum current rating is package limited.
5. Guaranteed by design, not subject to production testing.

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TYPICAL CHARACTERISTIC CURVES

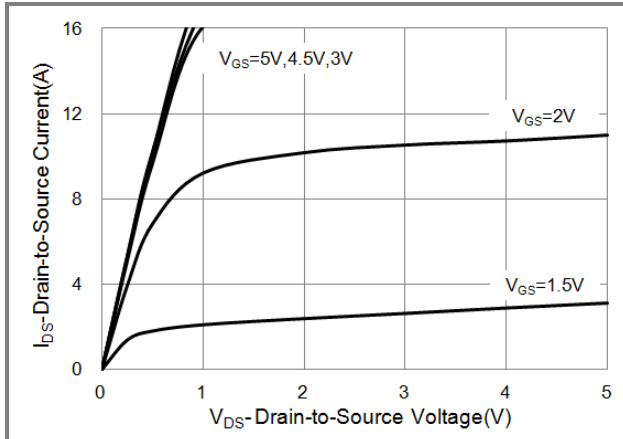


Fig.1 On-Region Characteristics

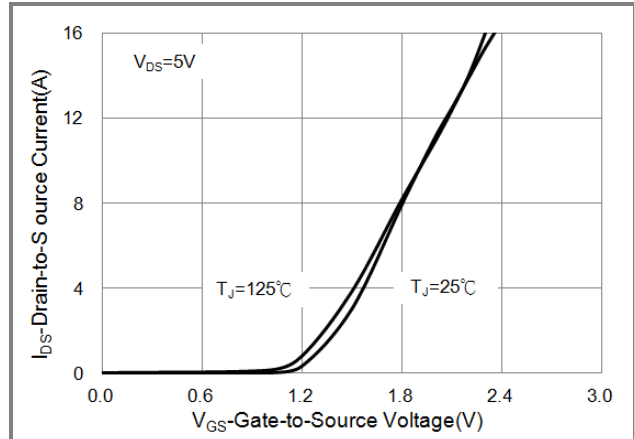


Fig.2 Transfer Characteristics

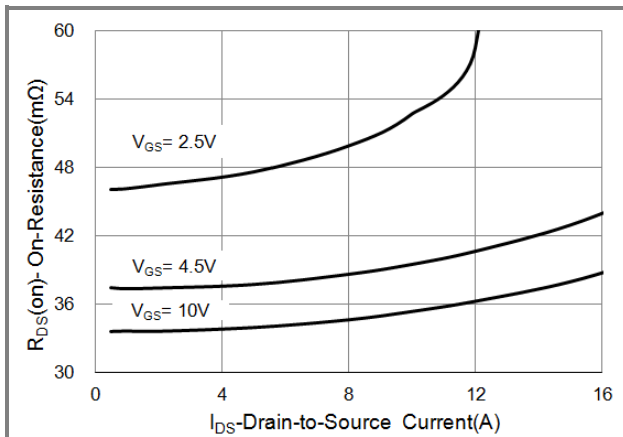


Fig.3 On-Resistance vs. Drain Current

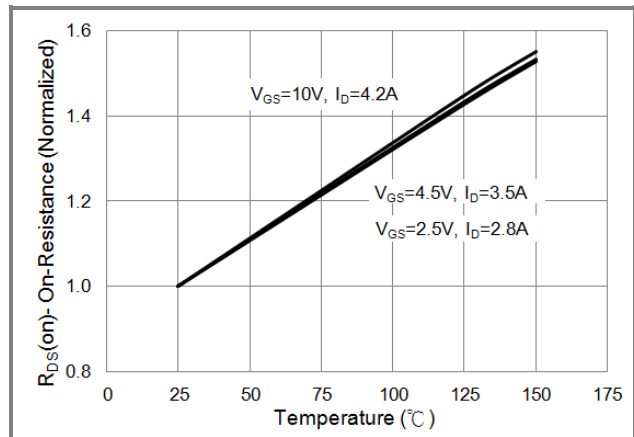


Fig.4 On-Resistance vs. Junction temperature

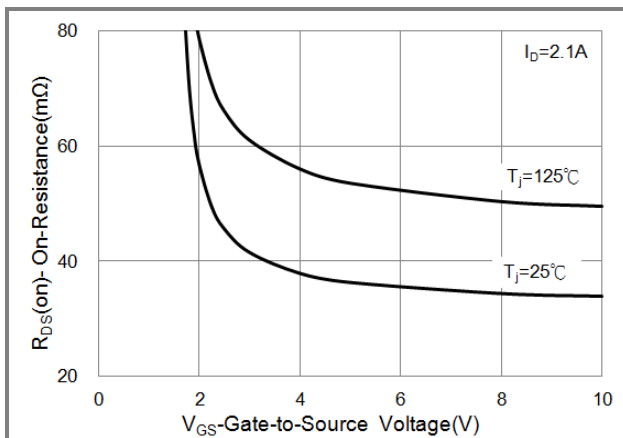


Fig.5 On-Resistance Variation with VGS.

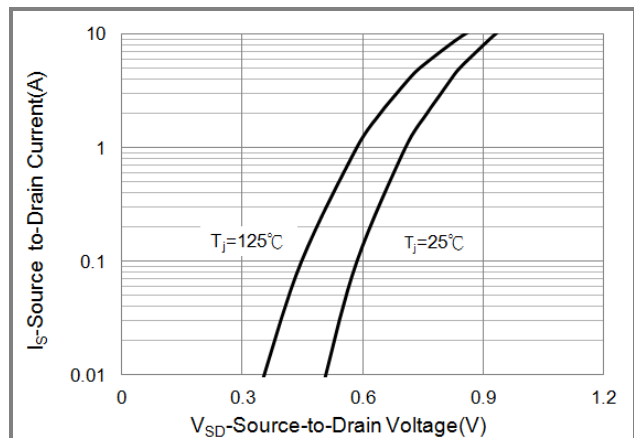


Fig.6 Body Diode Characteristics

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TYPICAL CHARACTERISTIC CURVES

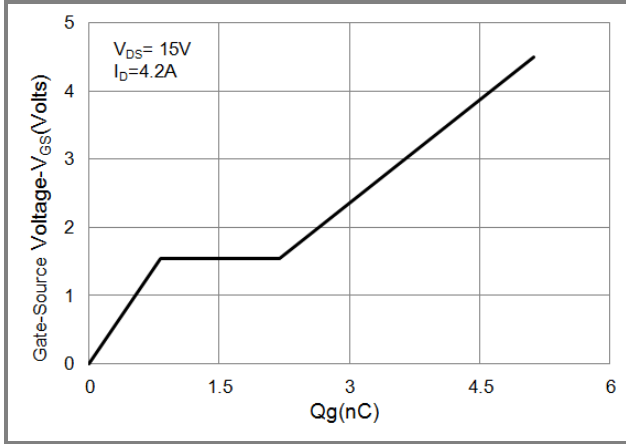


Fig.7 Gate-Charge Characteristics

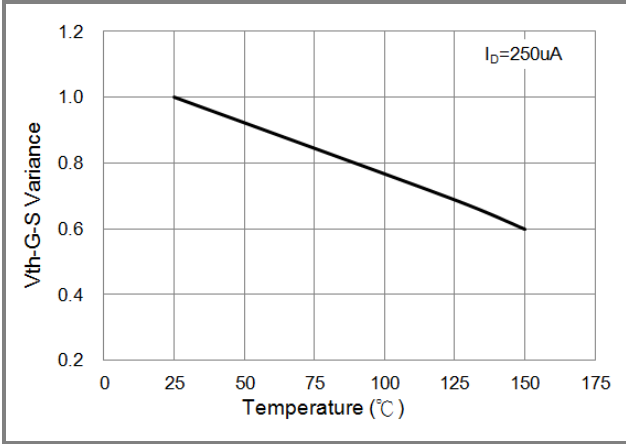


Fig.8 Threshold Voltage Variation with Temperature

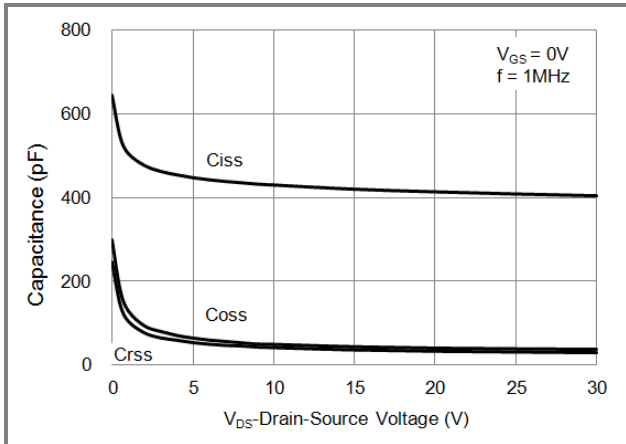


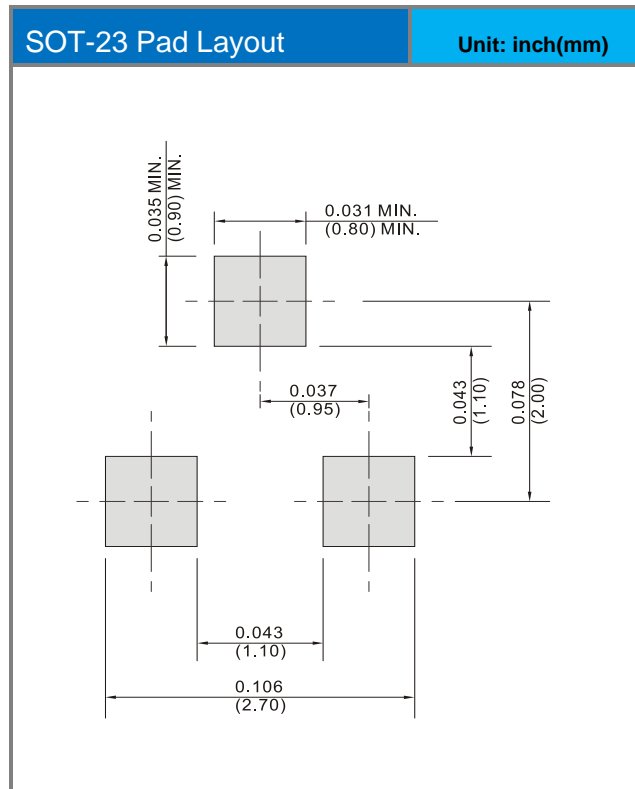
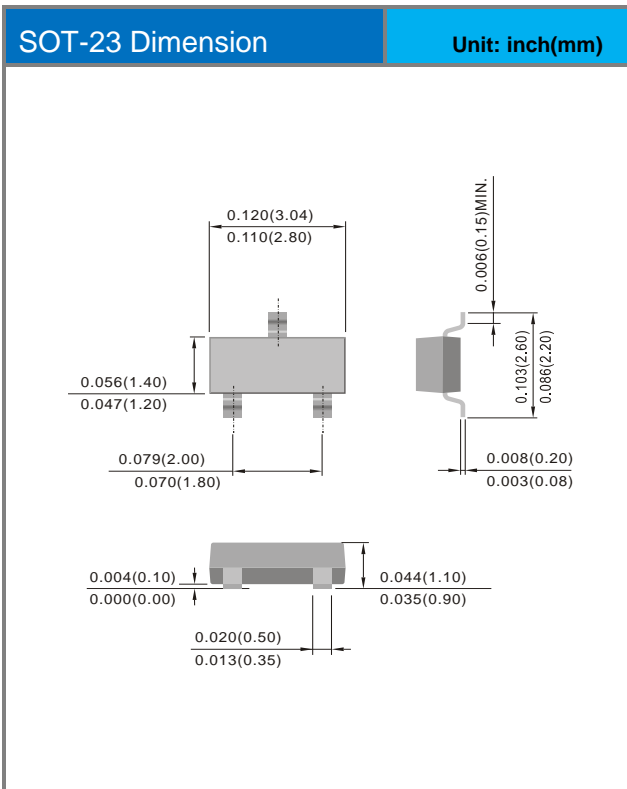
Fig.9 Capacitance vs. Drain-Source Voltage.

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Product and Packing Information

Part No.	Package Type	Packing Type	Marking
PJA3424E	SOT-23	3K pcs / 7" reel	24E

Packaging Information & Mounting Pad Layout



PJA3424E

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