NUJIT SEMI CONDUCTOR PJA3433		
30V P-Channel Enhancement Mode MOSFET – E		
Voltage -30 V Current -1.1A	SOT-23	Unit: inch(mm z
Features	0.120(3.04)	008(0:15)MIN
RDS(ON) , VGS@-4.5V, ID@-1.1A<370mΩ	+	
RDS(ON) , VGS@-2.5V, ID@-0.5A<540mΩ	Ţ	0.086(2.20)
RDS(ON) , VGS@-1.8V, ID@-0.1A<970mΩ	0.056(1.40) 0.047(1.20)	0.08
Advanced Trench Process Technology	0.079(2.00)	0.008(0.20)
Specially Designed for Switch Load, PWM Application, etc.	0.070(1.80)	0.003(0.08)
ESD Protected 2KV HBM	0.004(0.10)	0.044(1.10)
Lead free in compliance with EU RoHS 2.0	0.000(0.00)	0.035(0.90)
Green molding compound as per IEC 61249 standard	0.020(0.50) 0.013(0.35)	
Mechanical Data		<u></u>
Case : SOT-23 Package		<u>*</u>
Terminals : Solderable per MIL-STD-750, Method 2026		

• Approx. Weight : 0.0003 ounces, 0.0084 grams

#### **Maximum Ratings and Thermal Characteristics** (T<sub>A</sub>=25°C unless otherwise noted)

PARAMETER		SYMBOL	LIMIT	UNITS
Drain-Source Voltage		V <sub>DS</sub>	-30	V
Gate-Source Voltage		V <sub>GS</sub>	<u>+</u> 8	V
Continuous Drain Current		lь	-1.1	А
Pulsed Drain Current (Note 4)		I <sub>DM</sub>	-4.4	А
Power Dissipation	T₂=25°C	Po	1.25	W
	Derate above 25°C		10	mW/°C
Operating Junction and Storage Temperature Range		TJ,TSTG	-55~150	٥C
Typical Thermal resistance - Junction to Ambient <sup>(Note 3)</sup>		R <sub>0JA</sub>	100	°C/W

1 G 2

S



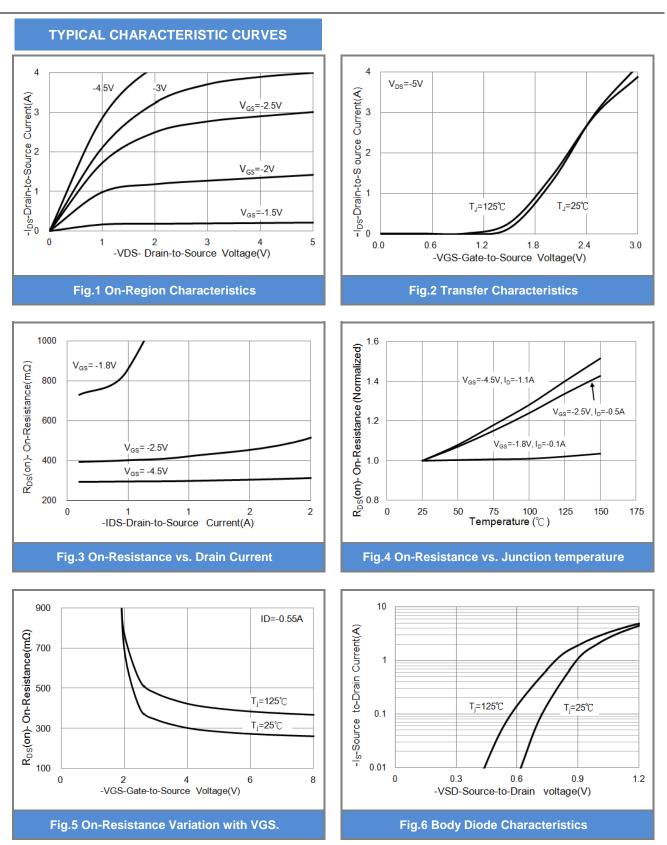
#### Electrical Characteristics (T<sub>A</sub>=25°C unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS	
Static							
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V, I <sub>D</sub> =-250uA	-30	-	-	V	
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =-250uA	-0.5	-0.98	-1.3	V	
Drain-Source On-State Resistance		V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-1.1A	-	293	370	_	
	R <sub>DS(on)</sub>	V <sub>GS</sub> =-2.5V, I <sub>D</sub> =-0.5A	-	387	540	mΩ	
		V <sub>GS</sub> =-1.8V, I <sub>D</sub> =-0.1A	-	750	970	1	
Zero Gate Voltage Drain Current	IDSS	V <sub>DS</sub> =-30V, V <sub>GS</sub> =0V	-	-0.01	-1	uA	
Gate-Source Leakage Current	lgss	V <sub>GS</sub> = <u>+</u> 8V, V <sub>DS</sub> =0V	-	<u>+</u> 3.4	<u>+</u> 10	uA	
Dynamic (Note 5)							
Total Gate Charge	Qg		-	1.6	-	nC	
Gate-Source Charge	Qgs	V <sub>DS</sub> =-15V, I <sub>D</sub> =-1.1A,	-	0.5	-		
Gate-Drain Charge	$Q_{gd}$	V <sub>GS</sub> =-4.5V <sup>(Note 1,2)</sup>	-	0.3	-		
Input Capacitance	Ciss		-	125	-		
Output Capacitance	Coss	V <sub>DS</sub> =-15V, V <sub>GS</sub> =0V,	-	22	-	pF	
Reverse Transfer Capacitance	Crss	f=1.0MHZ	-	6	-		
Turn-On Delay Time	td <sub>(on)</sub>		-	11	-		
Turn-On Rise Time	tr	V <sub>DD</sub> =-15V, I <sub>D</sub> =-1.1A,	-	51	-		
Turn-Off Delay Time	td <sub>(off)</sub>	$V_{GS}=-4.5V$ ,	-	65	-	ns	
Turn-Off Fall Time	tf	R <sub>G</sub> =6Ω <sup>(Note 1,2)</sup>	-	46	-		
Drain-Source Diode							
Maximum Continuous Drain-Source Diode Forward Current	ls		-	-	-1.0	A	
Diode Forward Voltage	V <sub>SD</sub>	I <sub>S</sub> =-1.0A, V <sub>GS</sub> =0V	-	-0.9	-1.2	V	

NOTES :

- 1. Pulse width</br>
- 2. Essentially independent of operating temperature typical characteristics.
- 3. R<sub>0JA</sub> is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins mounted on a 1 inch FR-4 with 2oz. square pad of copper.
- 4. The maximum current rating is package limited.
- 5. Guaranteed by design, not subject to production testing.







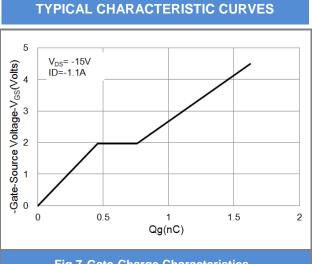


Fig.7 Gate-Charge Characteristics

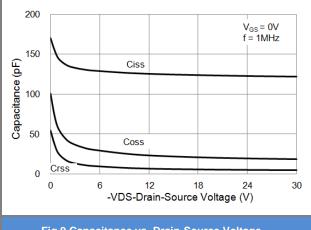


Fig.9 Capacitance vs. Drain-Source Voltage.

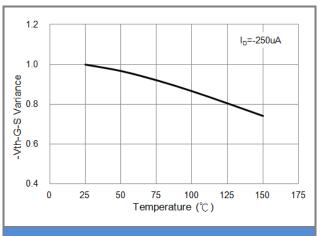


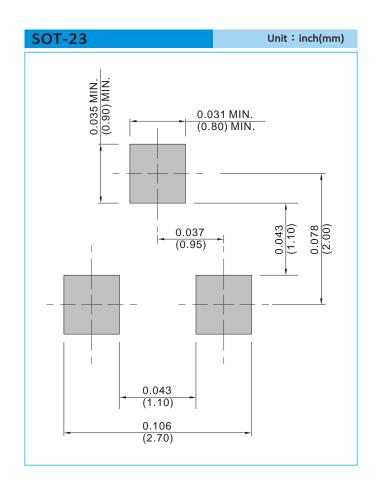
Fig.8 Threshold Voltage Variation with Temperature.



#### **Product and Packing Information**

Part No.	Package Type	ckage Type Packing Type	
PJA3433	SOT-23	3K pcs / 7" reel	A33

#### **Mounting Pad Layout**





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