



# PJD10P10A

## 100V P-Channel Enhancement Mode MOSFET

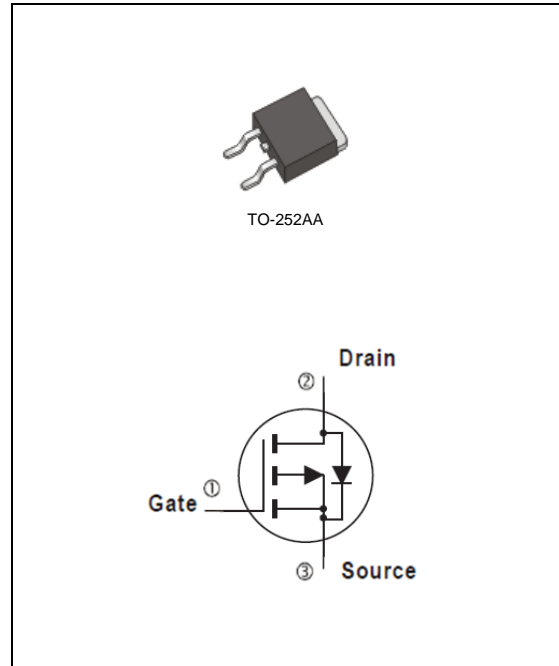
**Voltage**    **-100 V**    **Current**    **-10 A**

### Features

- $R_{DS(ON)}, V_{GS}@-10V, I_D@-5A < 210m\Omega$
- $R_{DS(ON)}, V_{GS}@-4.5V, I_D@-3A < 230m\Omega$
- High switching speed
- Improved dv/dt capability
- Low Gate Charge
- Low reverse transfer capacitance
- Lead free in compliance with EU RoHS 2011/65/EU directive.
- Green molding compound as per IEC61249 Std..  
(Halogen Free)

### Mechanical Data

- Case : TO-252AA Package
- Terminals : Solderable per MIL-STD-750, Method 2026
- Approx. Weight : 0.0104 ounces, 0.297grams



### Maximum Ratings and Thermal Characteristics (T<sub>A</sub>=25°C unless otherwise noted)

PARAMETER		SYMBOL	LIMIT	UNITS
Drain-Source Voltage		V <sub>DS</sub>	-100	V
Gate-Source Voltage		V <sub>GS</sub>	+20	V
Continuous Drain Current	T <sub>C</sub> =25°C	I <sub>D</sub>	-10	A
	T <sub>C</sub> =100°C		-6	
Pulsed Drain Current (Note 1)	T <sub>C</sub> =25°C	I <sub>DM</sub>	-40	
Power Dissipation	T <sub>C</sub> =25°C	P <sub>D</sub>	54	W
	T <sub>C</sub> =100°C		22	
Continuous Drain Current	T <sub>A</sub> =25°C	I <sub>D</sub>	-2.0	A
	T <sub>A</sub> =70°C		-1.6	A
Power Dissipation	T <sub>A</sub> =25°C	P <sub>D</sub>	2.0	W
Power Dissipation	T <sub>A</sub> =70°C		1.3	
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>STG</sub>	-55~150	°C
Typical Thermal Resistance (Note 4,5)	Junction to Case	R <sub>θJC</sub>	2.3	°C/W
	Junction to Ambient	R <sub>θJA</sub>	62.5	

- Limited only By Maximum Junction Temperature



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## Electrical Characteristics (T<sub>A</sub>=25°C unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
<b>Static</b>						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V, I <sub>D</sub> =-250uA	-100	-	-	V
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =-250uA	-1.0	-1.9	-3.0	V
Drain-Source On-State Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> =-10V, I <sub>D</sub> =-5A	-	170	210	mΩ
		V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-3A	-	190	230	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =-100V, V <sub>GS</sub> =0V	-	-	-1.0	uA
Gate-Source Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V	-	-	±100	nA
<b>Dynamic</b> (Note 6)						
Total Gate Charge	Q <sub>g</sub>	V <sub>DS</sub> =-80V, I <sub>D</sub> =-5A, V <sub>GS</sub> =-10V (Note 1,2)	-	20	-	nC
Gate-Source Charge	Q <sub>gs</sub>		-	3.5	-	
Gate-Drain Charge	Q <sub>gd</sub>		-	4.6	-	
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> =-25V, V <sub>GS</sub> =0V, f=1.0MHZ	-	1419	-	pF
Output Capacitance	C <sub>oss</sub>		-	89	-	
Reverse Transfer Capacitance	C <sub>rss</sub>		-	45	-	
Turn-On Delay Time	t <sub>d(on)</sub>	V <sub>DS</sub> =-50V, I <sub>D</sub> =-5A, V <sub>GS</sub> =-10V, R <sub>G</sub> =25Ω (Note 1,2)	-	18	-	ns
Turn-On Rise Time	t <sub>r</sub>		-	8	-	
Turn-Off Delay Time	t <sub>d(off)</sub>		-	100	-	
Turn-Off Fall Time	t <sub>f</sub>		-	30	-	
<b>Drain-Source Diode</b>						
Maximum Continuous Drain-Source Diode Forward Current	I <sub>S</sub>	---	-	-	-10	A
Reverse Recovery Time	V <sub>SD</sub>	I <sub>S</sub> =-1A, V <sub>GS</sub> =0V	-	-0.74	-1.2	V

**NOTES :**

1. Pulse width ≤ 300us, Duty cycle ≤ 2%
2. Essentially independent of operating temperature typical characteristics
3. Repetitive rating, pulse width limited by junction temperature T<sub>J</sub>(MAX)=150°C. Ratings are based on low frequency and duty cycles to keep initial T<sub>J</sub> = 25°C.
4. The maximum current rating is package limited
5. R<sub>θJA</sub> is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. Mounted on a 1 inch<sup>2</sup> with 2oz. square pad of copper
6. Guaranteed by design, not subject to production testing.



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## TYPICAL CHARACTERISTIC CURVES

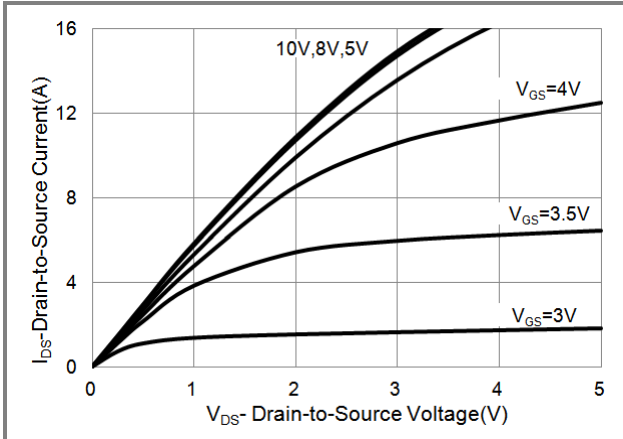


Fig.1 On-Region Characteristics

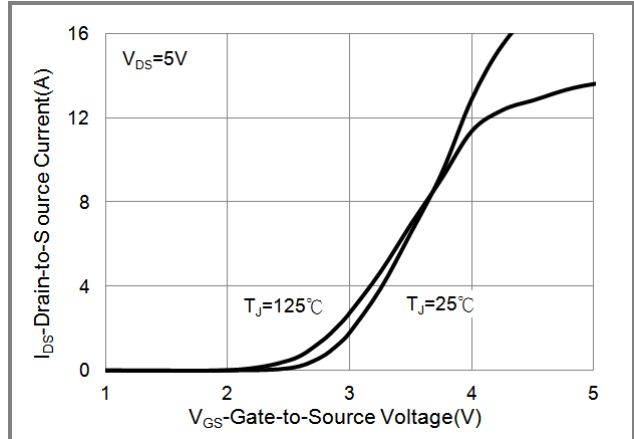


Fig.2 Transfer Characteristics

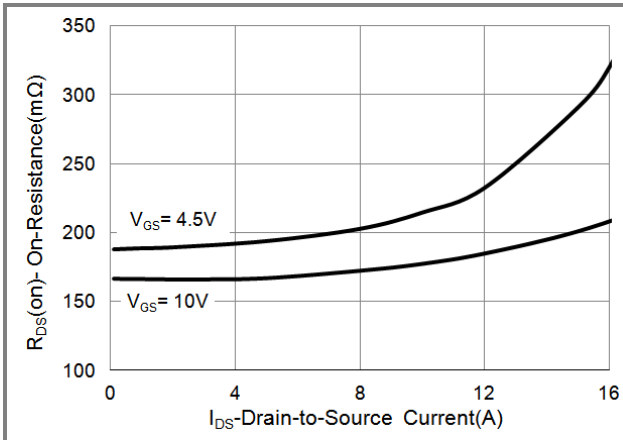


Fig.3 On-Resistance vs. Drain Current

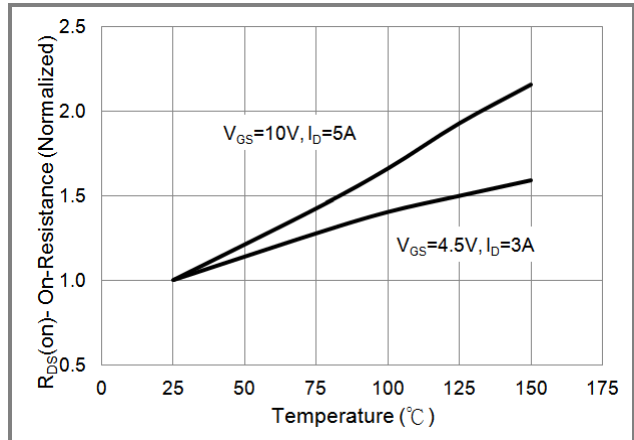


Fig.4 On-Resistance vs. Junction temperature

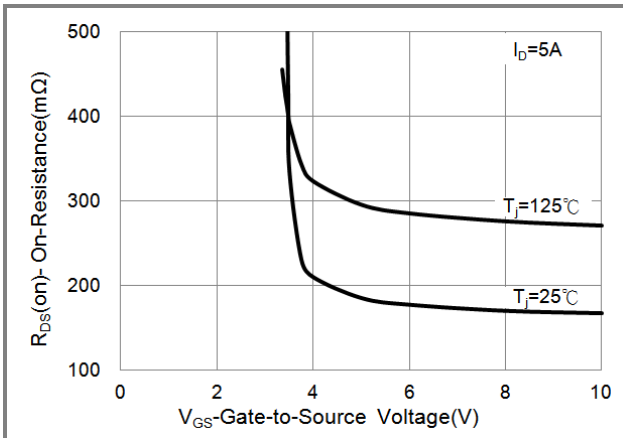


Fig.5 On-Resistance Variation with VGS.

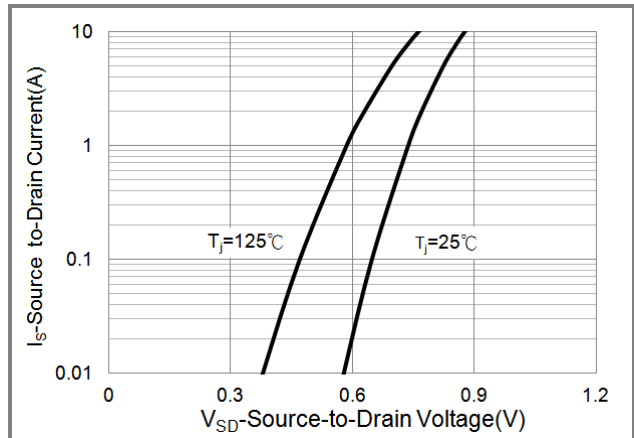


Fig.6 Body Diode Characteristics



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## TYPICAL CHARACTERISTIC CURVES

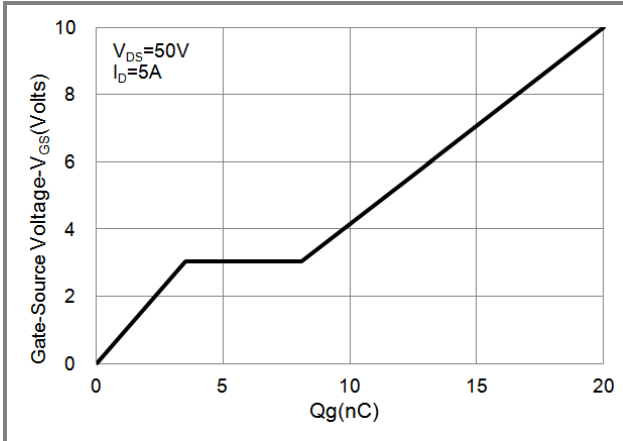


Fig.7 Gate-Charge Characteristics

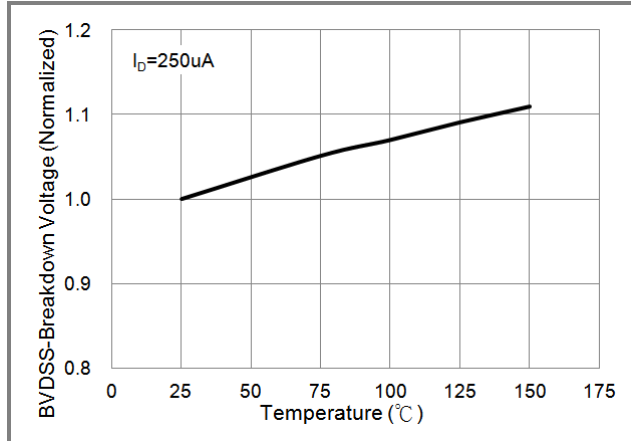


Fig.8 Breakdown Voltage Variation vs. Temperature

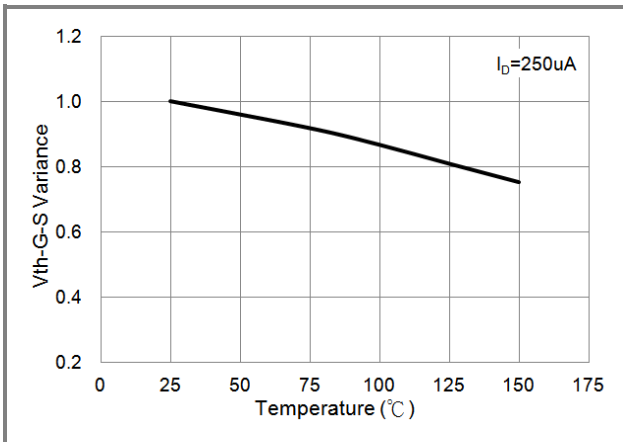


Fig.9 Threshold Voltage Variation with Temperature

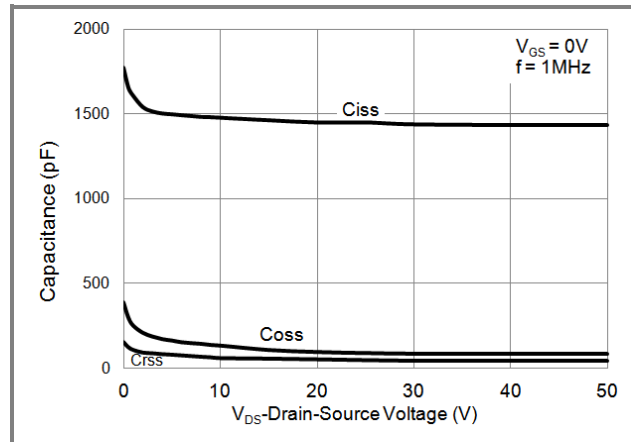


Fig.10 Capacitance vs. Drain-Source Voltage

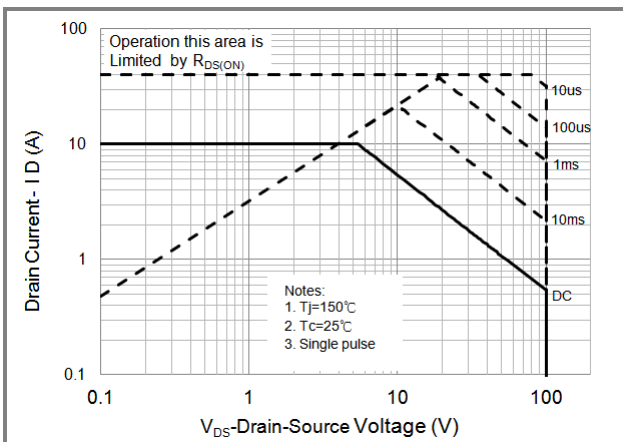
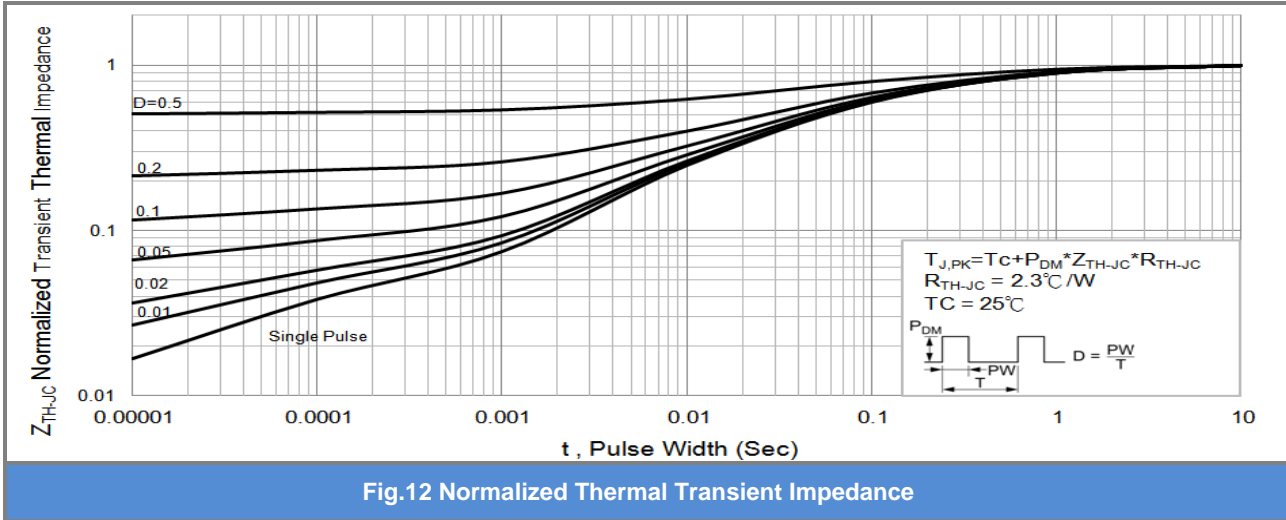


Fig.11 Maximum Safe Operating Area



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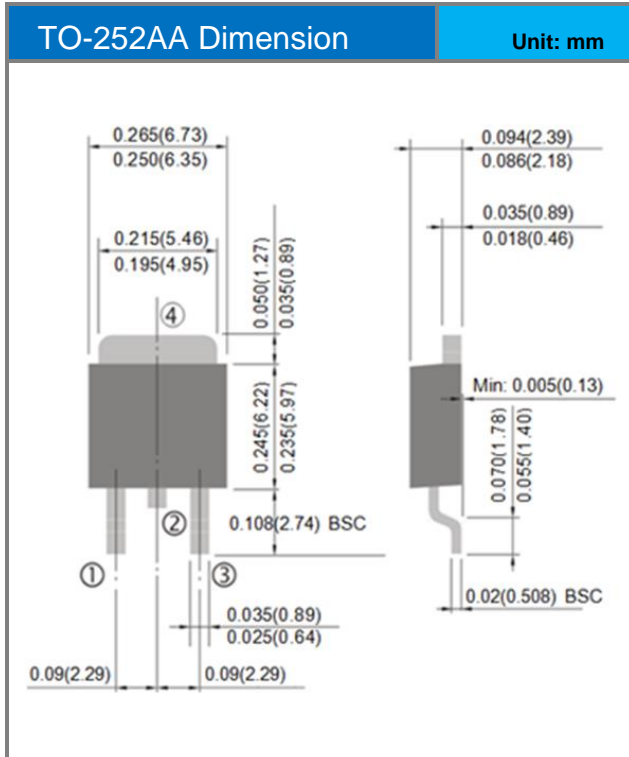
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## Packaging Information



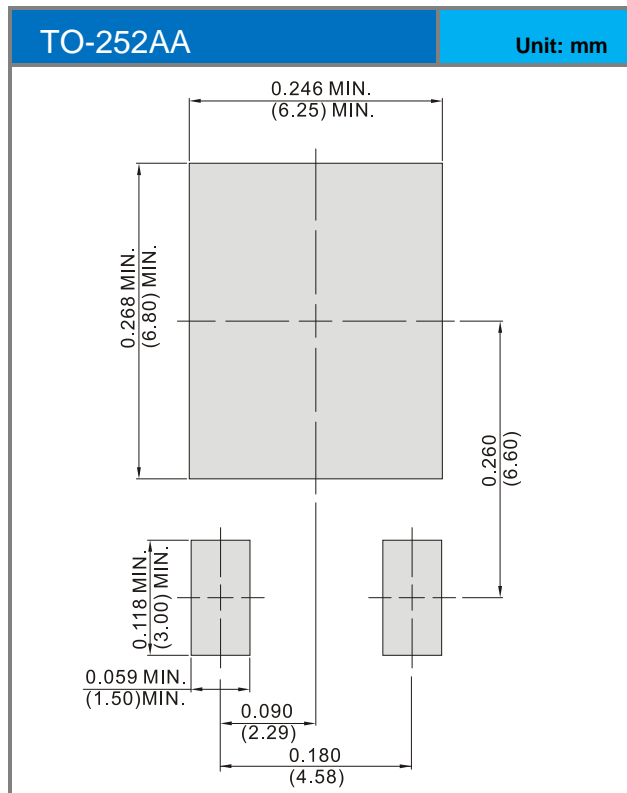


# PJD10P10A

## PART NO. PACKING CODE VERSION

Part No. Packing Code	Package Type	Packing Type	Marking	Version
PJD10P10A_L2_00001	TO-252AA	3,000pcs / 13" reel	D10P10A	Halogen free

## MOUNTING PAD LAYOUT





## **PJD10P10A**

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