



PJT7839-AU

60V P-Channel Enhancement Mode MOSFET

Voltage **-60 V** **Current** **-250mA**

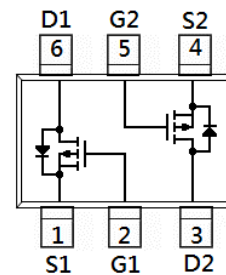
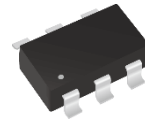
Features

- RDS(ON) , VGS@-10V, ID@-500mA<4Ω
- RDS(ON) , VGS@-4.5V, ID@-200mA<6Ω
- RDS(ON) , VGS@-2.5V, ID@-50mA<13Ω
- Advanced Trench Process Technology
- Specially Designed for Relay driver, Speed line drive, etc.
- AEC-Q101 qualified
- Lead free in compliance with EU RoHS 2.0
- Green molding compound as per IEC 61249 standard

Mechanical Data

- Case : SOT-363 Package
- Terminals : Solderable per MIL-STD-750, Method 2026
- Approx. Weight : 0.0002 ounces, 0.006 grams

SOT-363



Maximum Ratings and Thermal Characteristics (T_A=25°C unless otherwise noted)

PARAMETER	SYMBOL	LIMIT	UNITS
Drain-Source Voltage	V _{DS}	-60	V
Gate-Source Voltage	V _{GS}	+20	V
Continuous Drain Current	I _D	-250	mA
Pulsed Drain Current ^(Note 1)	I _{DM}	-1000	mA
Power Dissipation	P _D	T _A =25°C	350
		Derate above 25°C	2.8
Operating Junction and Storage Temperature Range	T _J , T _{STG}	-55~150	°C
Typical Thermal Resistance	R _{θJA}	357	°C/W
- Junction to Ambient ^(Note 3)			



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Electrical Characteristics (T_A=25°C unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V, I _D =-250uA	-60	-	-	V
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _D =-250uA	-1.0	-1.5	-2.5	V
Drain-Source On-State Resistance	R _{DS(on)}	V _{GS} =-10V, I _D =-500mA	-	2.4	4	Ω
		V _{GS} =-4.5V, I _D =-200mA	-	2.65	6	
		V _{GS} =-2.5V, I _D =-50mA	-	4.5	13	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =-48V, V _{GS} =0V	-	-	-1	uA
Gate-Source Leakage Current	I _{GSS}	V _{GS} =±20V, V _{DS} =0V	-	-	±100	nA
Dynamic (Note 4)						
Total Gate Charge	Q _g	V _{DS} =-25V, I _D =-100mA, V _{GS} =-4.5V	-	1.1	-	nC
Gate-Source Charge	Q _{gs}		-	0.3	-	
Gate-Drain Charge	Q _{gd}		-	0.2	-	
Input Capacitance	C _{iss}	V _{DS} =-25V, V _{GS} =0V, f=1.0MHZ	-	51	-	pF
Output Capacitance	C _{oss}		-	15	-	
Reverse Transfer Capacitance	C _{rss}		-	2.2	-	
Turn-On Delay Time	t _{d(on)}	V _{DD} =-25V, I _D =-100mA, V _{GS} =-10V, R _G =6Ω (Note 1,2)	-	4.8	-	ns
Turn-On Rise Time	t _r		-	19	-	
Turn-Off Delay Time	t _{d(off)}		-	52	-	
Turn-Off Fall Time	t _f		-	32	-	
Drain-Source Diode						
Maximum Continuous Drain-Source Diode Forward Current	I _S	---	-	-	-250	mA
Diode Forward Voltage	V _{SD}	I _S =-500mA, V _{GS} =0V	-	-0.95	-1.3	V

NOTES :

1. Pulse width ≤ 300us, Duty cycle ≤ 2%
2. Essentially independent of operating temperature typical characteristics.
3. R_{θJA} is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. mounted on a 1 inch square pad of copper
4. Guaranteed by design, not subject to production testing



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TYPICAL CHARACTERISTIC CURVES

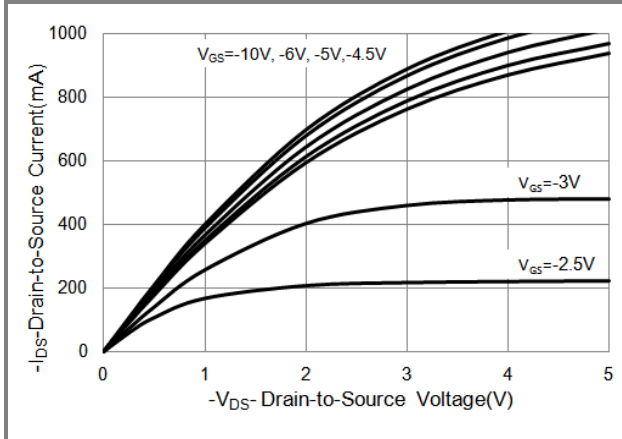


Fig.1 On-Region Characteristics

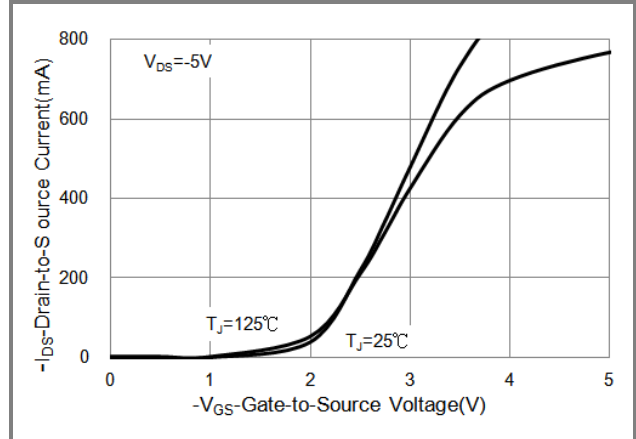


Fig.2 Transfer Characteristics

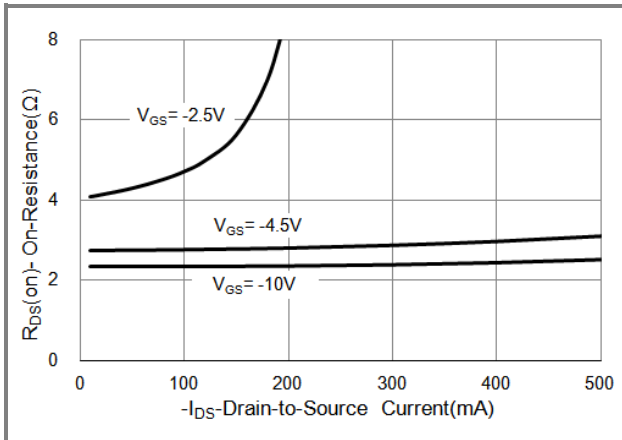


Fig.3 On-Resistance vs. Drain Current

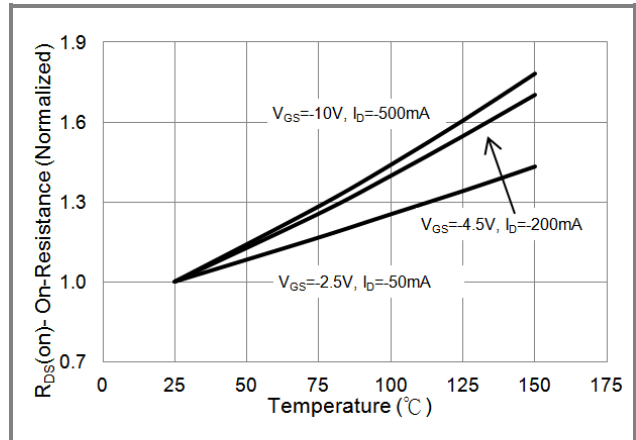


Fig.4 On-Resistance vs. Junction temperature

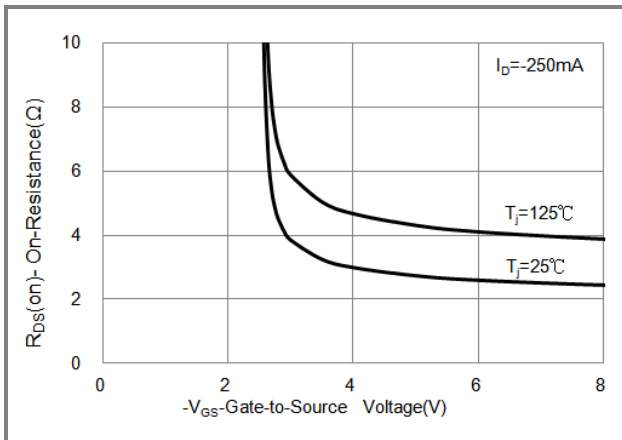


Fig.5 On-Resistance Variation with VGS.

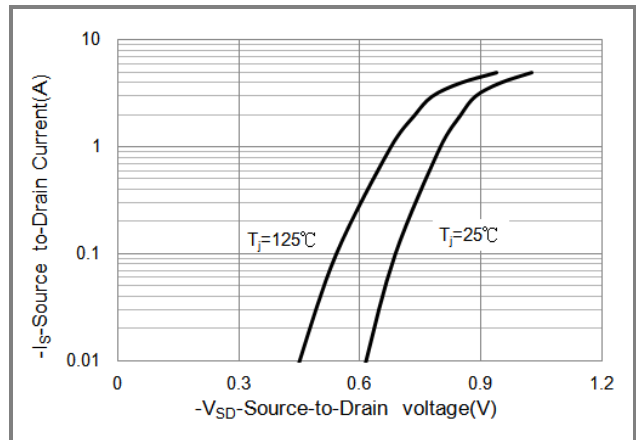


Fig.6 Body Diode Characteristics



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TYPICAL CHARACTERISTIC CURVES

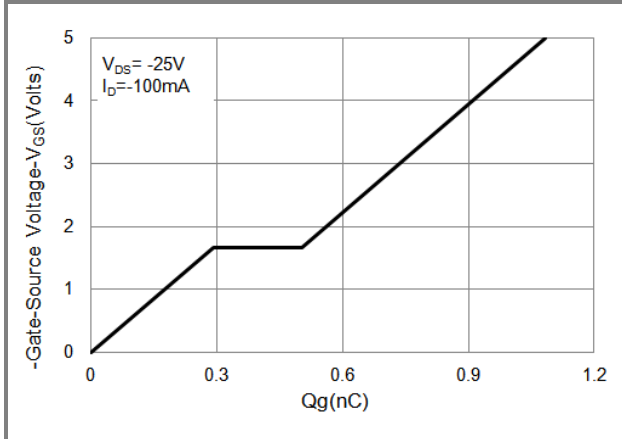


Fig.7 Gate-Charge Characteristics

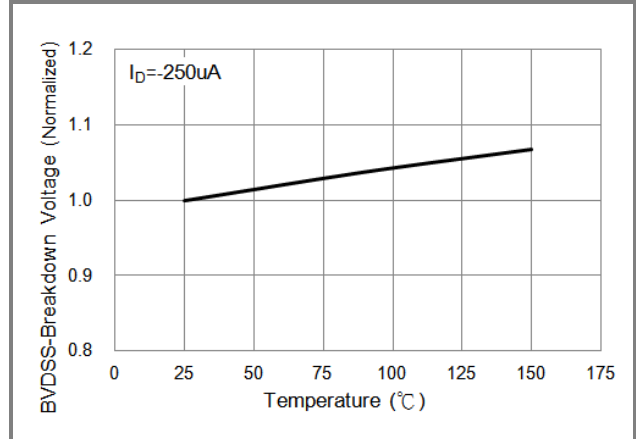


Fig.8 Breakdown Voltage Variation vs. Temperature

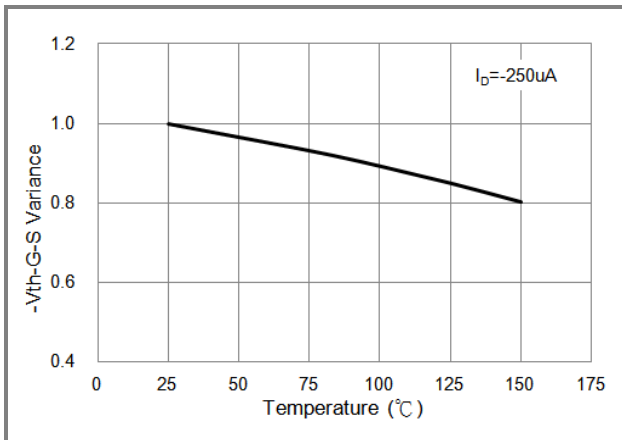


Fig.9 Threshold Voltage Variation with Temperature.

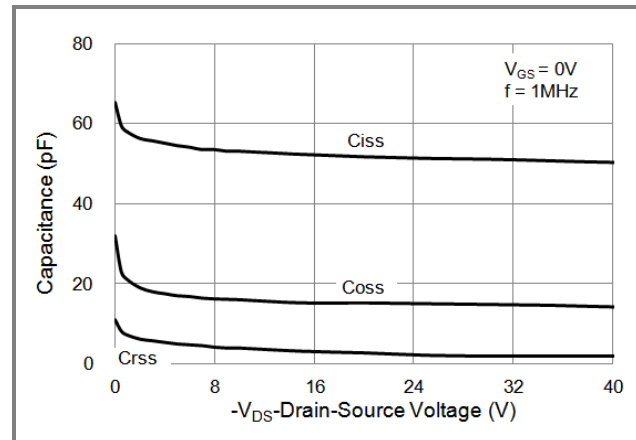


Fig.10 Capacitance vs. Drain-Source Voltage.

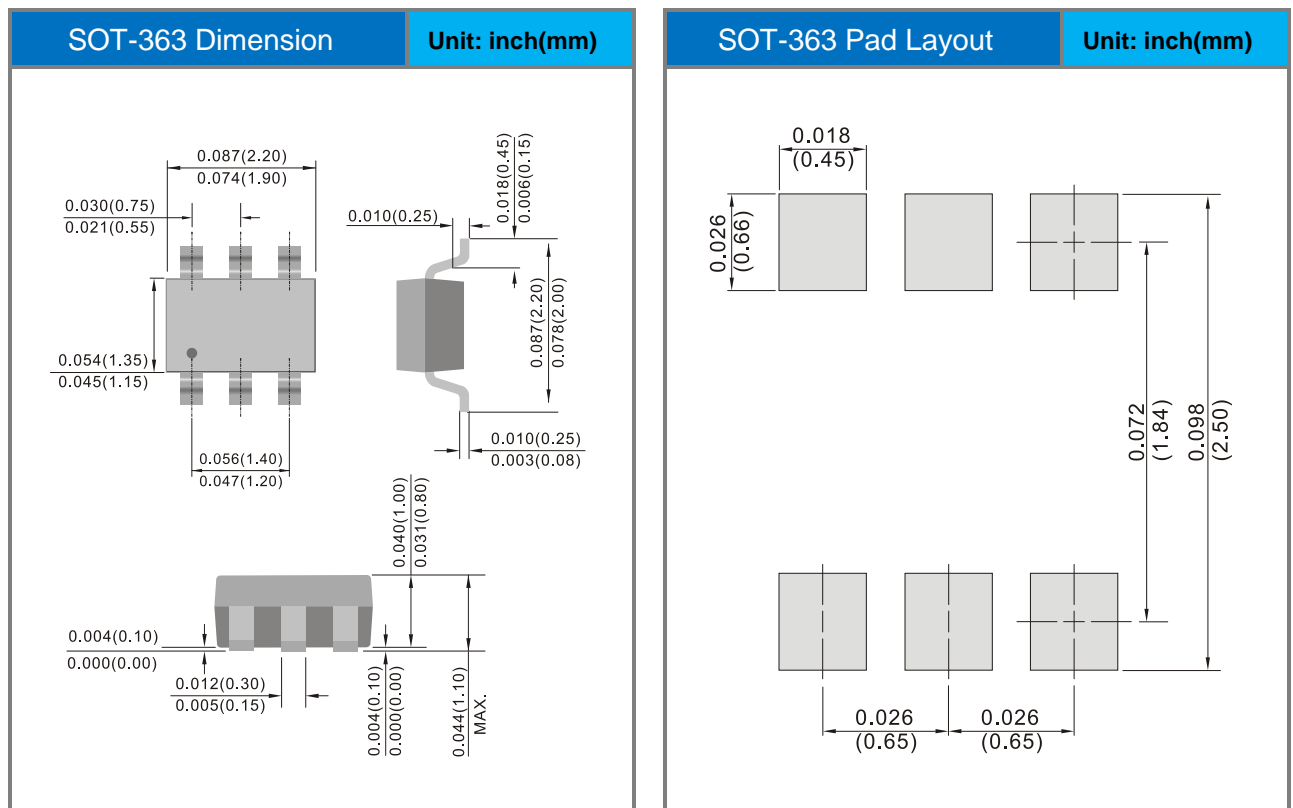


PJT7839-AU

Part No. Packing Code Version

Part No. Packing Code	Package Type	Packing Type	Marking	Version
PJT7839-AU_R1_000A1	SOT-363	3K pcs / 7" reel	T39	Halogen free RoHS compliant

Packaging Information & Mounting Pad Layout





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