

650V N-Channel Super Junction MOSFET

Voltage	650 V	Rdson	60mΩ
Current	58.3 A	Qg	100.3nC

Feature:

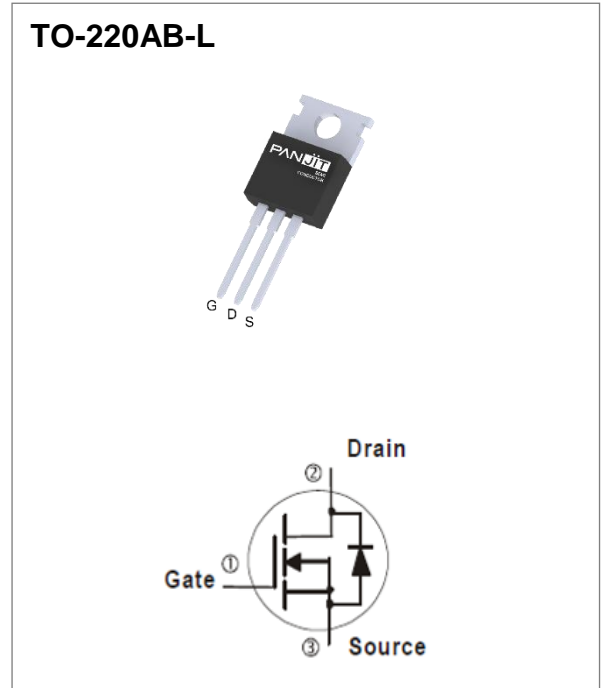
- $R_{DS(ON) Max, V_{GS}@10V}$: 60mΩ
- Body diode with fast recovery characteristics
- High Speed Switching and Low $R_{DS(ON)}$
- 100% Avalanche Tested
- 100% Rg Tested
- Lead free in compliance with EU RoHS 2.0
- Green molding compound as per IEC 61249 standard

Mechanical Data

- Case: TO-220AB-L package
- Terminals: Solderable per MIL-STD-750, Method 2026
- Approx. Weight: 2.0948 grams

Application

- PFC/ DC-DC Primary FET of PSU/ UPS / ESS PCS



Absolute Maximum Ratings ($T_A = 25^\circ\text{C}$ unless otherwise specified)

PARAMETER		SYMBOL	LIMIT	UNITS
Drain-Source Voltage @ T_{jmax}		V_{DS}	700	V
Drain-Source Voltage		V_{DS}	650	
Gate-Source Voltage		V_{GS}	± 30	
Continuous Drain Current	$T_C=25^\circ\text{C}$	I_D	58.3	A
	$T_C=100^\circ\text{C}$		36.9	
Pulsed Drain Current	$T_C=25^\circ\text{C}$	I_{DM}	128	A
Single Pulse Avalanche Energy		E_{AS}	304	mJ
MOSFET dv/dt ruggedness		dv/dt	120	V/ns
Diode dv/dt		dv/dt	70	V/ns
Power Dissipation	$T_C=25^\circ\text{C}$	Pd	500	W
	$T_C=100^\circ\text{C}$		200	
Operating Junction and Storage Temperature Range		T_J, T_{STG}	-55~150	$^\circ\text{C}$

Thermal Characteristics

PARAMETER	SYMBOL	VALUES			UNITS	
		MIN.	TYP.	MAX.		
Thermal Resistance	Junction-to-Case (Bottom)	$R_{\theta JC}$	-	0.18	0.25	$^\circ\text{C/W}$
	Junction-to-Ambient (Note 3)	$R_{\theta JA}$	-	30	50	$^\circ\text{C/W}$

Electrical Characteristics (T_A = 25 °C unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V, I _D =250uA	650	-	-	V
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _D =250uA	3.3	4.0	4.7	
Drain-Source On-State Resistance	R _{DS(on)}	V _{GS} =10V, I _D =23A (Note 1)	-	47.4	60	mΩ
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =650V, V _{GS} =0V	-	-	10	uA
Gate-Source Leakage Current	I _{GSS}	V _{GS} =±30V, V _{DS} =0V	-	-	±100	nA
Transfer characteristics	gfs	V _{DS} =20V, I _D =46A	-	50.6	-	S
Dynamic (Note 5)						
Total Gate Charge	Q _g	V _{DS} =400V, I _D =46A, V _{GS} =10V	-	100.3	-	nC
Gate-Source Charge	Q _{gs}		-	29.3	-	
Gate-Drain Charge	Q _{gd}		-	41.6	-	
Input Capacitance	C _{iss}	V _{DS} =400V, V _{GS} =0V, f=250kHz	-	4614	-	pF
Output Capacitance	C _{oss}		-	63.5	-	
Reverse Transfer Capacitance	C _{rss}		-	7	-	
Effective Output Capacitance Energy Related	Co(er)	V _{DS} =0V to 400V, V _{GS} =0V, f=250kHz (Note 4)	-	123.1	-	
Turn-On Delay Time	td(on)	V _{DD} =400V, I _D =46A, V _{GS} =10V, R _G =10Ω (Note 2)	-	55.3	-	ns
Turn-On Rise Time	t _r		-	181.3	-	
Turn-Off Delay Time	td(off)		-	113	-	
Turn-Off Fall Time	t _f		-	114	-	
Gate Resistance	R _g	f=1.0MHz	-	6	-	Ω
Drain-Source Diode						
Maximum Continuous Drain-Source Diode Forward Current	I _s		-	-	58.3	A
Diode Forward Voltage	V _{SD}	I _s =23A, V _{GS} =0V	-	0.90	1.5	V
Reverse Recovery Charge	Q _{rr}	I _s =46A di/dt=100A/μs	-	2.3	-	uC
Reverse Recovery Time	T _{rr}		-	154.3	-	ns
Reverse Recovery Current	I _{rrm}		-	12	-	A

NOTES :

1. Pulse width ≤ 380us, Duty cycle ≤ 2%.
2. Essentially independent of operating temperature typical characteristics.
3. R_{θJA} is the sum of the junction-to-case and case-to-ambient thermal resistance.
4. C_{o(er)} is a capacitance that gives the same stored energy as C_{oss} while V_{DS} is rising from 0V to 400V.
5. Guaranteed by design, not subject to production testing.
6. E_{AS} is calculated based on the condition of L = 10 mH, I_{AS} = 7.8 A, V_{DD} = 50 V, V_{GS} = 10 V, however, it's 100% test by I_{as}=7.5A & L=0.1mH in mass production.

TYPICAL CHARACTERISTIC CURVES

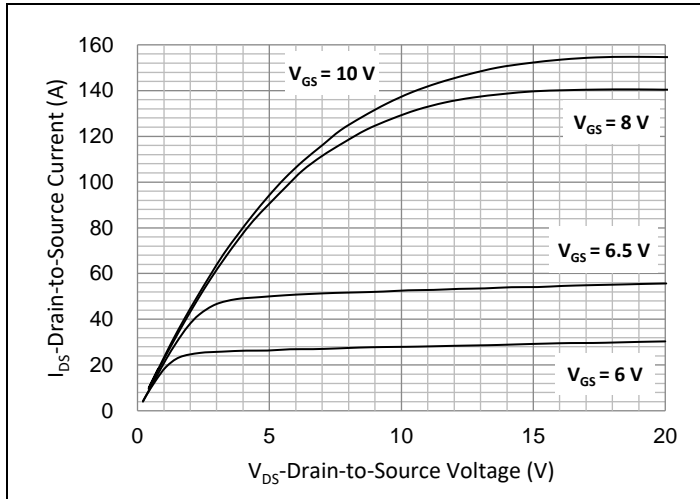


Fig.1 Output Characteristics

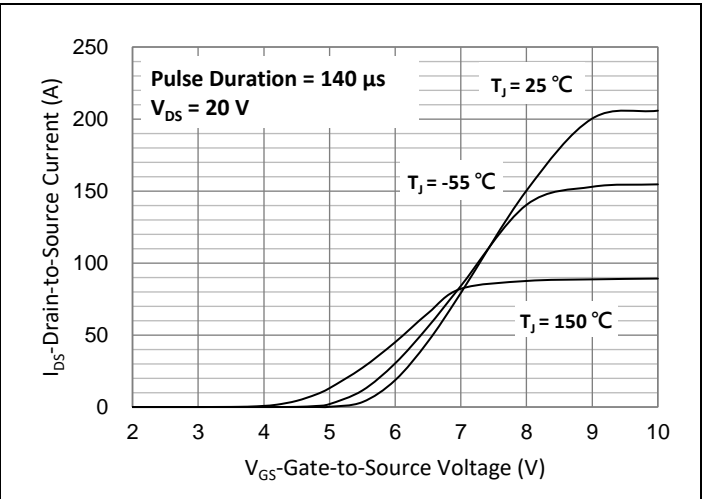


Fig.2 Transfer Characteristics

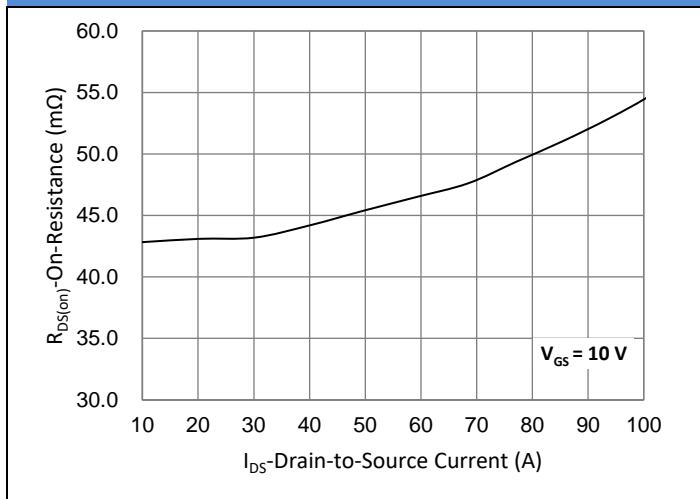


Fig.3 On-Resistance vs. Drain Current

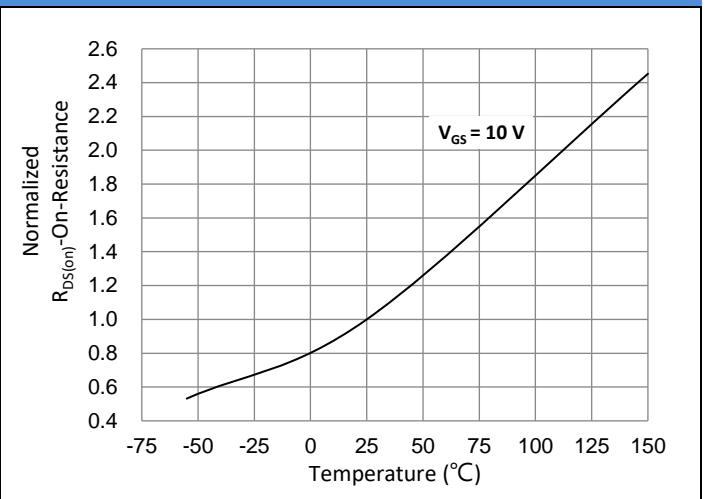


Fig.4 On-Resistance vs. Junction Temperature

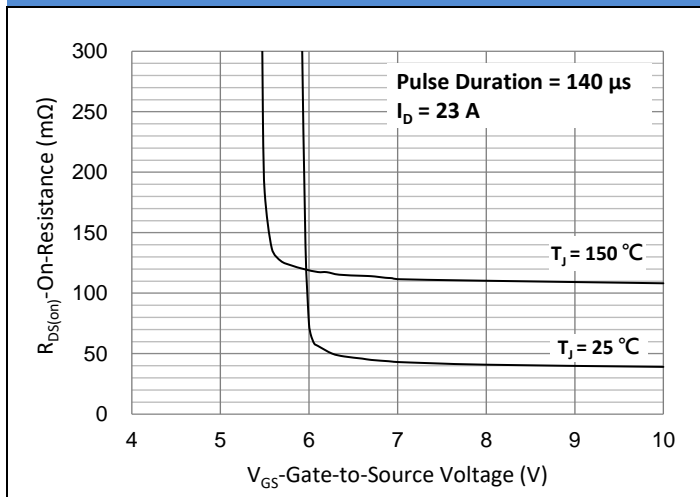


Fig.5 Capacitance vs. Drain-Source Voltage

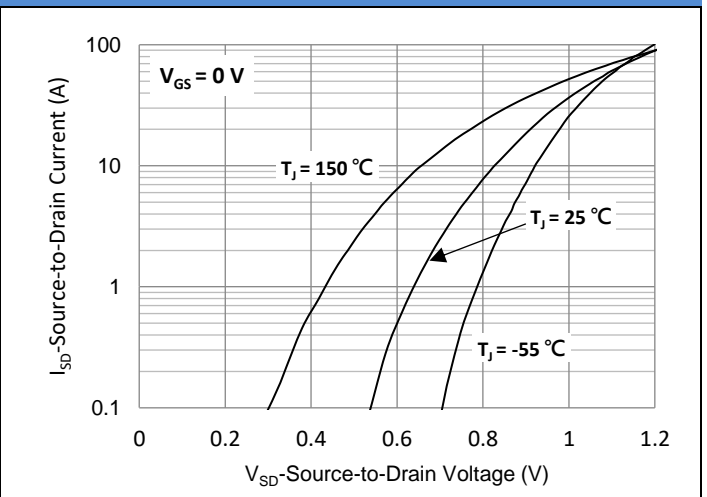


Fig.6 Source-Drain Diode Forward Voltage

TYPICAL CHARACTERISTIC CURVES

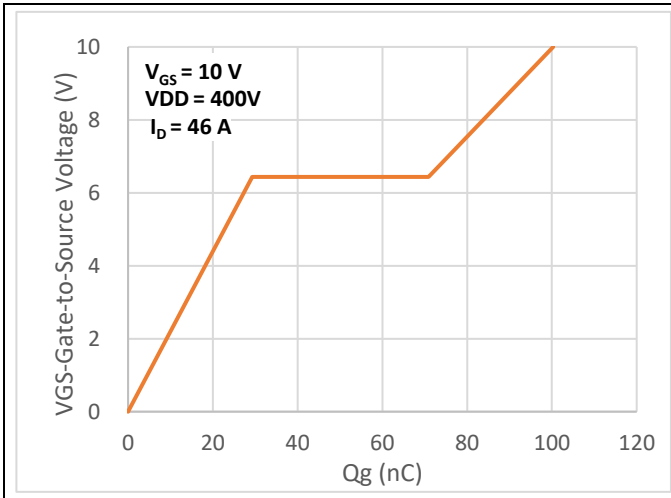


Fig.7 Gate-Charge Characteristics

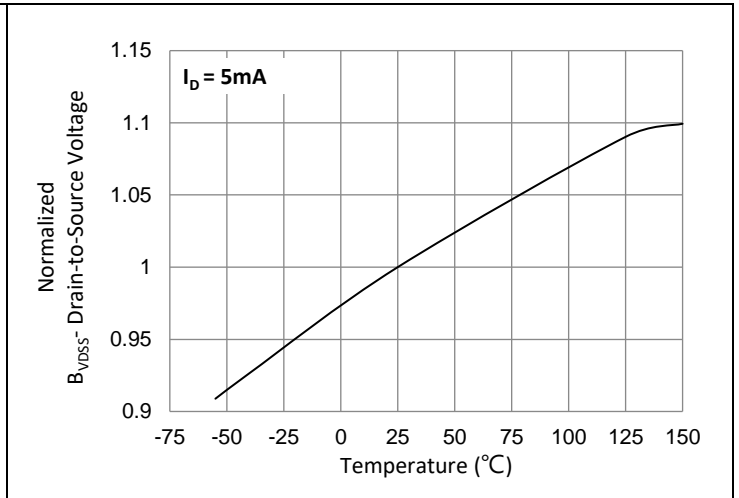


Fig.8 Breakdown Voltage Variation vs. Temperature

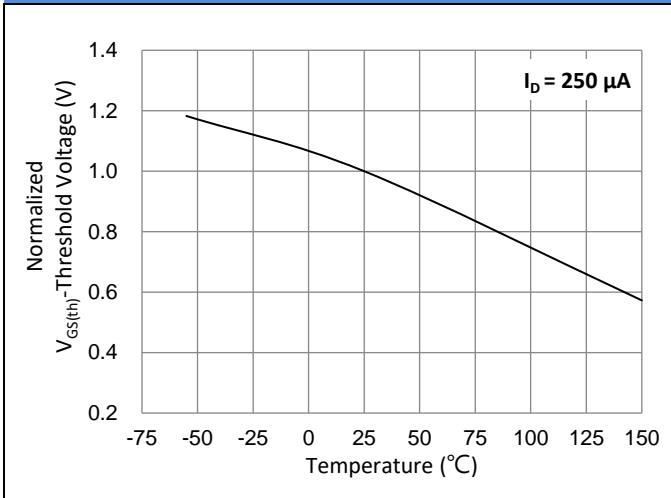


Fig.9 Threshold Voltage Variation with Temperature

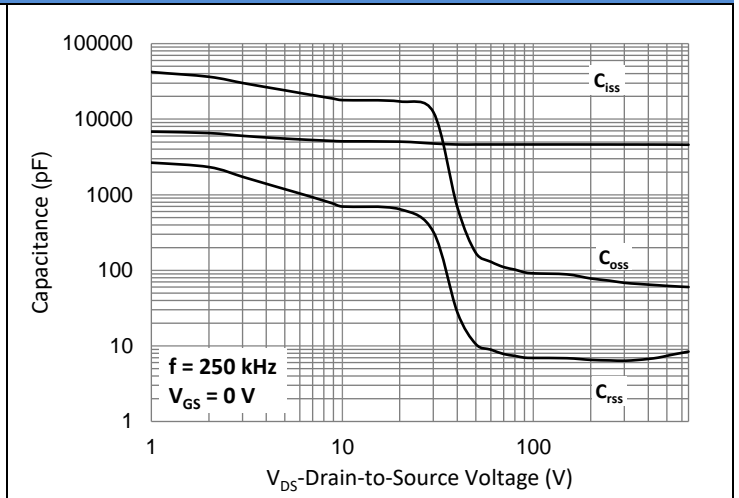


Fig.10 Drain Current vs. Case Temperature

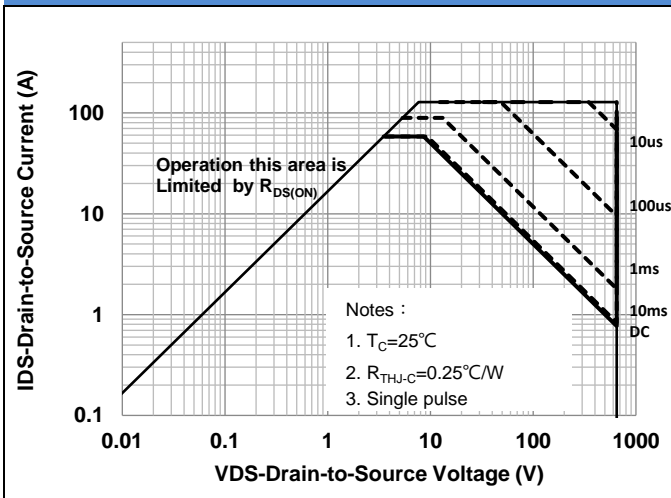


Fig.11 Maximum Safe Operating Area

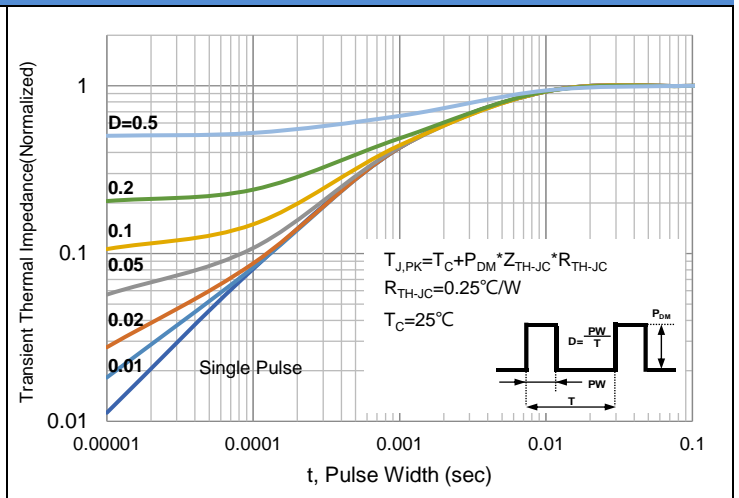


Fig.12 Normalized Transient Thermal Impedance

TYPICAL CHARACTERISTIC CURVES

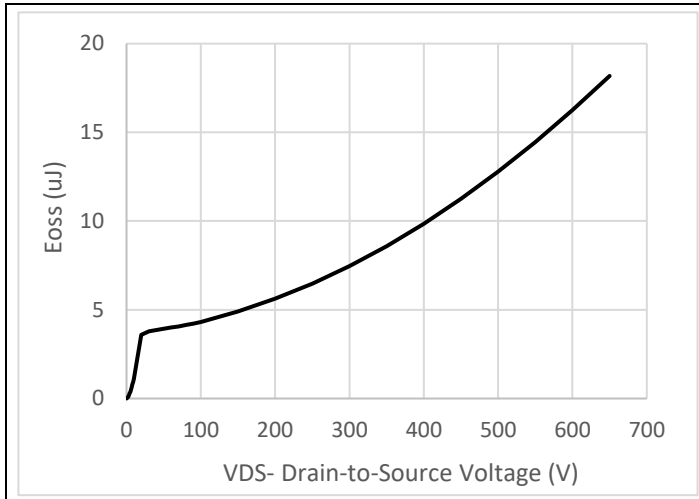
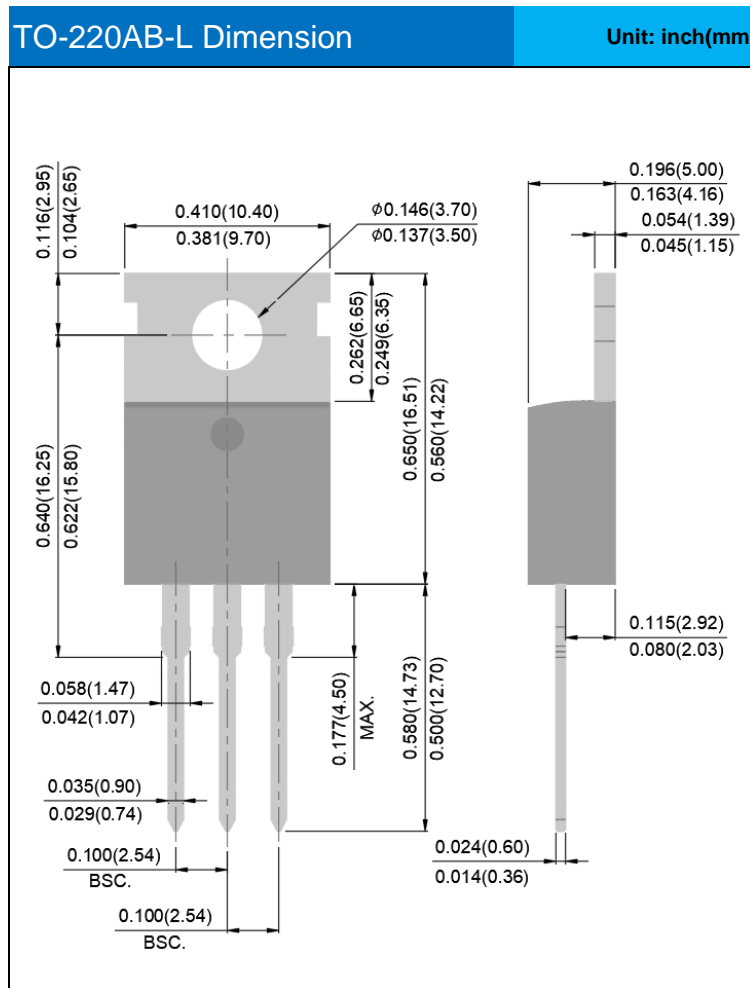


Fig.13 Typ. Coss Stored Energy

Product and Packing Information

Part No.	Package Type	Packing Type	Marking
PJMP060N65FR2	TO-220AB-L	50pcs / Tube	060N65FR2

Packaging Information



Marking Diagram

PJ
060N65FR2
YWLL x

- Y** = Year Code
- W** = Week Code (A~Z)
- LL** = Lot Code (00~99)
- x** = Production Line Code

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