

20V N-Channel Enhancement Mode MOSFET

Voltage

20 V

Current

800mA

Features

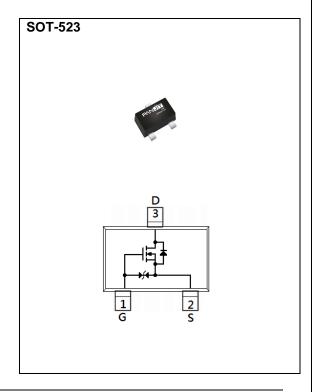
- Advanced Trench Process Technology
- ESD Protected
- Specially Designed for Switch Load, PWM Application, etc
- Lead free in compliance with EU RoHS 2.0
- Green molding compound as per IEC 61249 standard

Mechanical Data

• Case : SOT-523 Package

• Terminals : Solderable per MIL-STD-750, Method 2026

• Approx. Weight: 0.002 grams



Maximum Ratings and Thermal Characteristics (T_A=25°C unless otherwise noted)

PARAMETER		SYMBOL	LIMIT	UNITS	
Drain-Source Voltage		V _{DS}	20	V	
Gate-Source Voltage		V _{GS}	±8		
Continuous Drain Current(Note 4)		I _D	800	mA	
Pulsed Drain Current ^(Note 1)		I _{DM}	1600		
Power Dissipation	T _A =25°C		300	mW	
	Derate above 25°C	PD	2.4	mW/°C	
Operating Junction and Storage Temperature Range		T _J ,T _{STG}	-55~150	°C	
Thermal Resistance - Junction to Ambient ^(Note 5)		R ₀ JA	417	°C/W	



Electrical Characteristics (T_A=25°C unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS	
Static							
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V, I _D =250uA	20	-	-	V	
Gate Threshold Voltage	$V_{GS(th)}$	V _{DS} =V _{GS} , I _D =250uA	0.3	0.68	1		
Drain-Source On-State Resistance	R _{DS(on)}	V _{GS} =4.5V, I _D =500mA	V _{GS} =4.5V, I _D =500mA - 200		300		
		V _{GS} =2.5V, I _D =400mA -		240	400		
		V _{GS} =1.8V, I _D =200mA	-	300	550	mΩ	
		V _{GS} =1.5V, I _D =100mA	-	370	800		
		V _{GS} =1.2V, I _D =10mA	-	680	1500		
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =20V, V _{GS} =0V	-	-	1	uA	
Gate-Source Leakage Current	I _{GSS}	V _{GS} =±8V, V _{DS} =0V	-	-	±10		
Dynamic ^(Note 6)							
Total Gate Charge	Qg	.,	-	1.1	-	nC	
Gate-Source Charge	Qgs	V_{DS} =10V, I_{D} =500mA, V_{GS} =4.5V ^(Note 2,3)	-	0.2	-		
Gate-Drain Charge	Q_{gd}	VGS=4.5 V(Note 2,6)	-	0.1	-		
Input Capacitance	Ciss	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \	-	50	-		
Output Capacitance	Coss	V _{DS} =10V, V _{GS} =0V, f=1MHz	-	12	-	pF	
Reverse Transfer Capacitance	Crss	I=TIVIMZ	-	10	-		
Gate resistance	Rg	f=1.0MHz	-	1.6	-	Ω	
Turn-On Delay Time	td _(on)	.,	-	2	-		
Turn-On Rise Time	tr	V _{DS} =10V, I _D =500mA,	-	22	-	ns	
Turn-Off Delay Time	td(off)	V _{GS} =4.5V, R _G =3.3Ω	-	57	-		
Turn-Off Fall Time	tf	(**************************************	-	34	-		
Drain-Source Diode							
Diode Forward Current	Is		-	-	800	mA	
Diode Forward Voltage	V _{SD}	I _S =500mA,V _{GS} =0V	-	0.9	1	V	

NOTES:

- 1.Pulse width<300us, Duty cycle<2%.
- $2. Essentially \ independent \ of \ operating \ temperature \ typical \ characteristics.$
- $3. Repetitive\ rating,\ pulse\ width\ limited\ by\ junction\ temperature\ T_J(MAX) = 150^{\circ}C. Ratings\ are\ based\ on\ low\ frequency\ and\ duty\ cycles\ to\ keep\ initial\ T_J=25^{\circ}C.$
- 4. The maximum current rating is package limited.
- 5.ReJA is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. Mounted on a 1 inch² with 2oz.square pad of copper.

6. Guaranteed by design, not subject to production testing.



TYPICAL CHARACTERISTIC CURVES

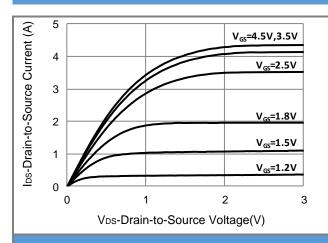


Fig.1 Output Characteristics

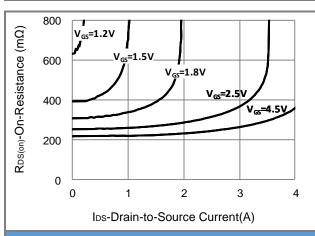


Fig.3 On-Resistance vs. Drain Current

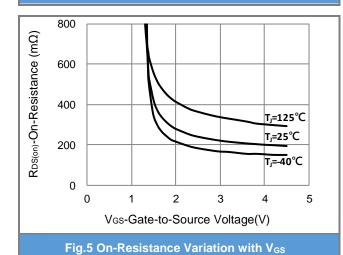


Fig.2 Transfer Characteristics

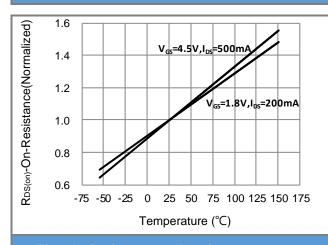


Fig.4 On-Resistance vs. Junction temperature

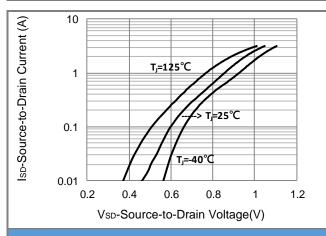


Fig.6 Source-Drain Diode Forward Voltage



TYPICAL CHARACTERISTIC CURVES

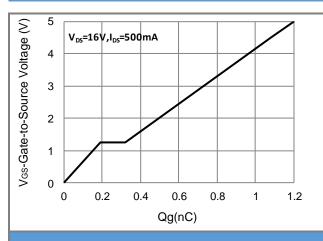


Fig.7 Gate-Charge Characteristics

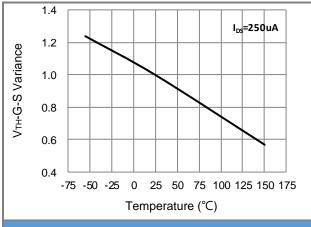


Fig.9 Threshold Voltage Variation with Temperature

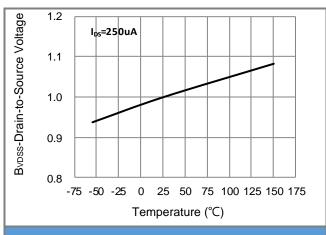


Fig.8 Breakdown Voltage Variation vs. Temperature

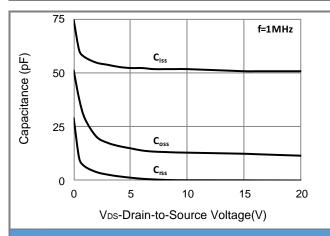


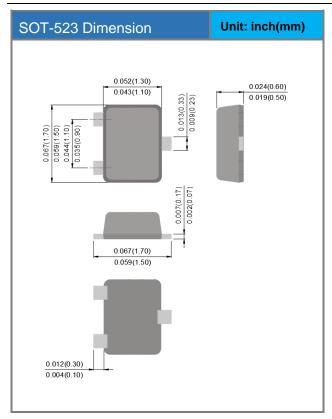
Fig.10 Capacitance vs. Drain-Source Voltage

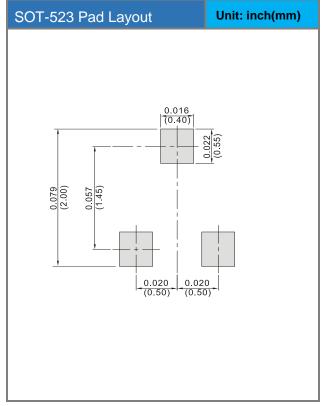


Product and Packing Information

Part No.	Package Type	Packing Type	Marking	
PJE8416	SOT-523	4K pcs / 7" reel	E16	

Packaging Information & Mounting Pad Layout







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