

PS8A12-DHA

Low Capacitance ESD Protection

Voltage

2.5V

Features

- IEC61000-4-2(ESD) : $\pm 30\text{kV}$ Air, $\pm 30\text{kV}$ Contact
- IEC61000-4-4(EFT) : 40A(5/50ns)
- IEC61000-4-5(Lightning) : 40A(8/20uS)
- Low leakage current, maximum of 1uA at rated voltage
- Ultra Low clamping voltage
- Lead free in compliance with EU RoHS 2.0
- Green molding compound as per IEC 61249 standard
- Pb-Free/Halogen Free/BFR Free and RoHS Compliant

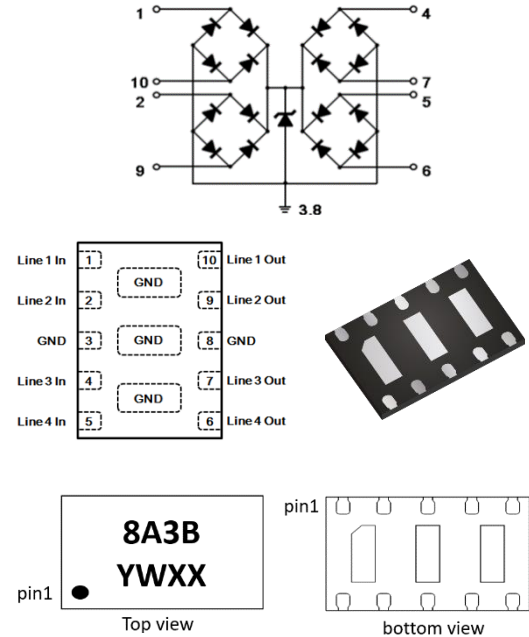
Mechanical Data

- Case : DFN3020-10L Package
- Terminals : Solderable per MIL-STD-750, Method 2026
- Approx. Weight : 0.0084 grams

Applications

- 10M/100M/1000M/2.5G Ethernet Ports
- Video Graphics Cards
- WAN/LAN Equipment
- Switching Systems
- Desktops Servers and Notebooks

DFN3020-10L



Part Marking	Parameter
8A3B	8A3B = Marking Code
YWXX	YWXX = Y - Last digit of calendar year W - Weekly XX - Tracking Code

Maximum Ratings and Thermal Characteristics (T_A = 25°C unless otherwise noted)

PARAMETER	SYMBOL	VALUE	UNITS
ESD IEC61000-4-2(Air)	V _{ESD}	± 30	kV
ESD IEC61000-4-2(Contact)		± 30	
Operating Junction Temperature Range	T _J	-55 to +125	°C
Storage Temperature Range	T _{STG}	-55 to +150	°C

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Electrical Characteristics ($T_A = 25^\circ\text{C}$ unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Reverse Stand-Off Voltage ^(Note 1)	V_{RWM}	I/O Pin to GND	-	-	2.5	V
Reverse Breakdown Voltage	V_{BR}	$I_{BR} = 1\text{mA}$, I/O Pin to GND	5	-	10	V
Forward Voltage	V_F	$I_F = 15\text{mA}$, GND to I/O Pin	-	1	-	V
Reverse Leakage Current	I_R	$V_R = 2.5\text{V}$, I/O Pin to GND	-	0.5	1	μA
Surge Clamping Voltage (8/20 μs)	V_{CL}	$I_{PP} = 40\text{A}$, Line to Ground, two I/O Pins connected together on each line	-	5.3	6.3	V
Clamping Voltage TLP ^(Note 2)	V_{CL}	$I_{TLP} = 16\text{A}$, Line to Ground, two I/O Pins connected together on each line	-	2	-	V
Off State Junction Capacitance (Note 3)	C_J	$V_R = 0\text{V}$, $f = 1\text{MHz}$, I/O Pins to Ground	-	1.5	2.5	pF

NOTES :

1. A transient suppressor is selected according to the working peak reverse voltage(V_{RWM}), which should be equal to or greater than the DC or continuous peak operation voltage level.
2. Testing using Transmission Line Pulse (TLP) conditions: $Z_0 = 50\Omega$, $t_P = 100\text{ ns}$.
3. This parameter is guaranteed by design.

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TYPICAL CHARACTERISTIC CURVES

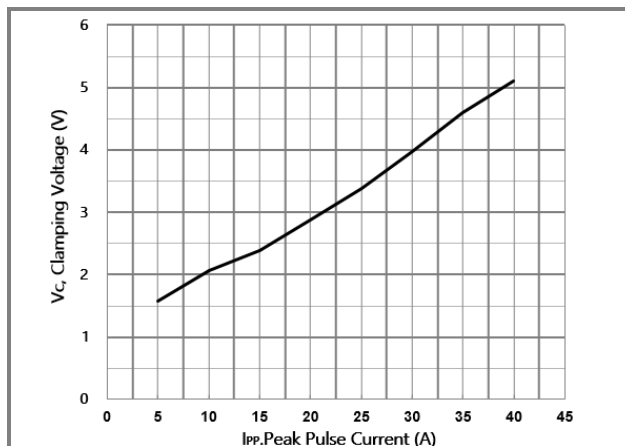


Fig.1 Typical Peak Clamping Voltage

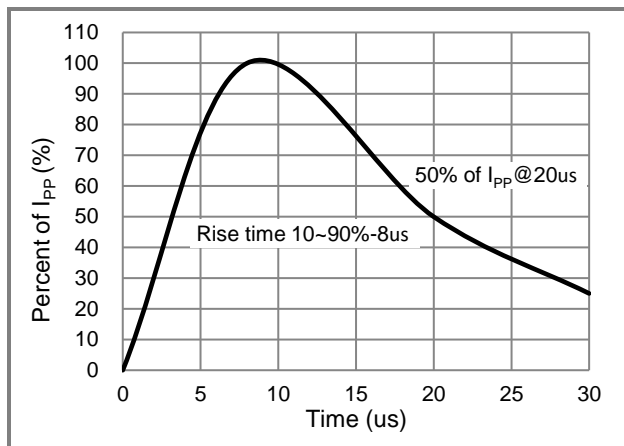


Fig.2 Pulse Waveform

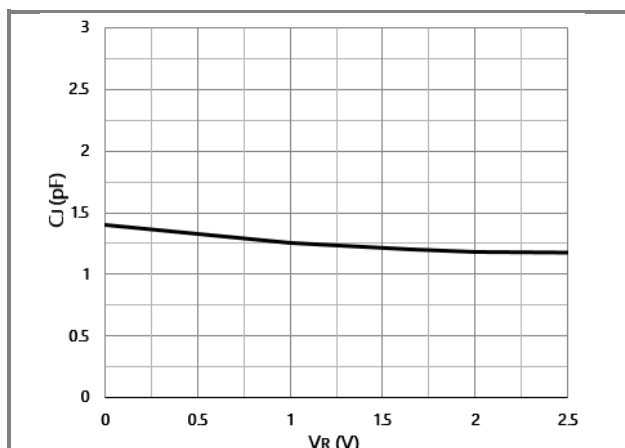


Fig.3 Typical Junction Capacitance

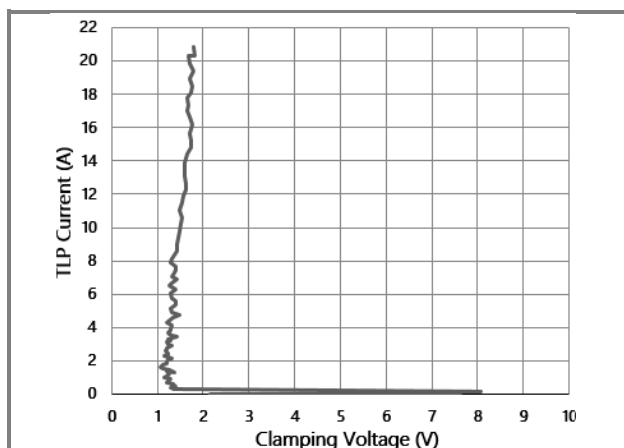


Fig.4 Positive TLP Measurement

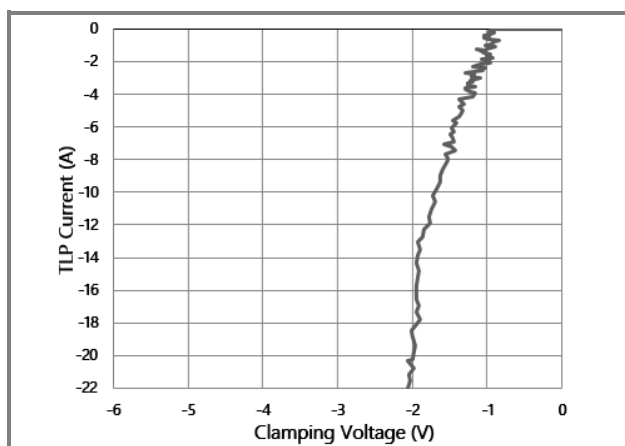


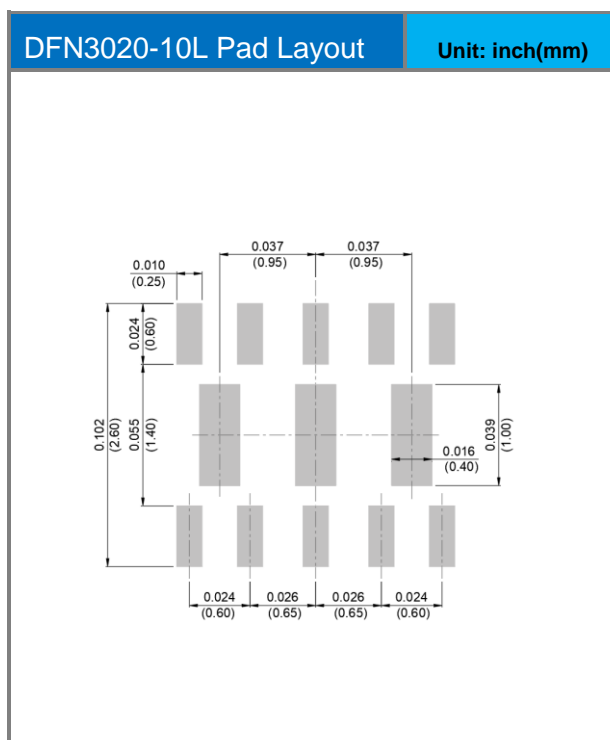
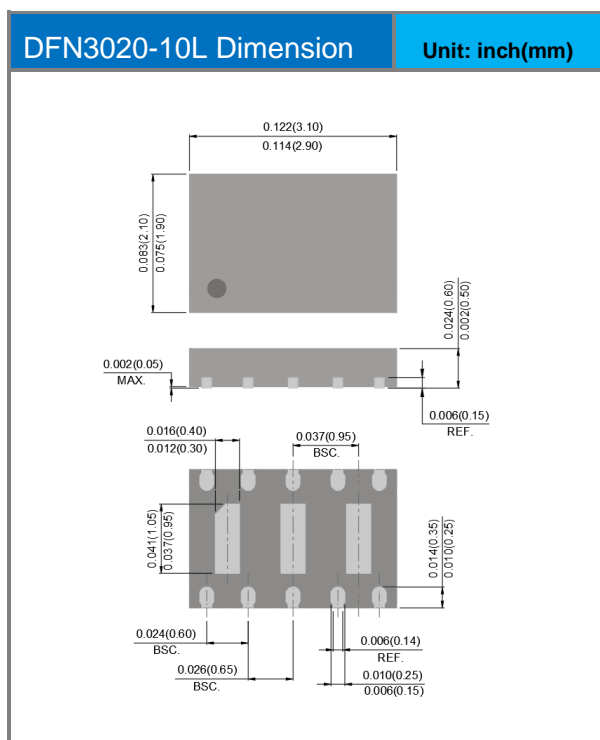
Fig.5 Negative TLP Measurement

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Product and Packing Information

Part No.	Package Type	Packing Type	Marking
PS8A12-DHA	DFN3020-10L	3K pcs / 7" reel	8A3B

Packaging Information & Mounting Pad Layout



PS8A12-DHA

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