

# PJA34817-AU

## 80V P-Channel Enhancement Mode MOSFET

<b>Voltage</b>	<b>-80 V</b>	<b>Current</b>	<b>-1.4 A</b>
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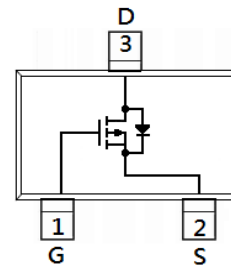
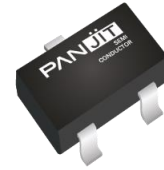
### Features

- $R_{DS(ON)}$  ,  $V_{GS}@-10V$ ,  $I_D@-1.2A < 360m\Omega$
- $R_{DS(ON)}$  ,  $V_{GS}@-6V$ ,  $I_D@-1.1A < 395m\Omega$
- Advanced Trench Process Technology
- Specially Designed for Switch Load, PWM Application, etc
- AEC-Q101 qualified
- Lead free in compliance with EU RoHS 2.0
- Green molding compound as per IEC 61249 standard

### Mechanical Data

- Case : SOT-23 Package
- Terminals : Solderable per MIL-STD-750, Method 2026
- Approx. Weight : 0.0084 grams

SOT-23



### Maximum Ratings and Thermal Characteristics ( $T_A=25^\circ C$ unless otherwise noted)

PARAMETER		SYMBOL	LIMIT	UNIT
Drain-Source Voltage		$V_{DS}$	-80	V
Gate-Source Voltage		$V_{GS}$	$\pm 20$	
Continuous Drain Current <sup>(Note 3)</sup>	$T_A=25^\circ C$	$I_D$	-1.4	A
	$T_A=70^\circ C$		-1.2	
Pulsed Drain Current <sup>(Note 1)</sup>	$T_A=25^\circ C$	$I_{DM}$	-5.6	
Power Dissipation	$T_A=25^\circ C$	$P_D$	1.5	W
	$T_A=70^\circ C$		1.05	
Operating Junction and Storage Temperature Range		$T_J, T_{STG}$	-55~175	$^\circ C$
Thermal Resistance <sup>(Note 3,4)</sup>	Junction to Ambient	$R_{\theta JA}$	100	$^\circ C/W$

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## Electrical Characteristics (T<sub>A</sub>=25°C unless otherwise noted)

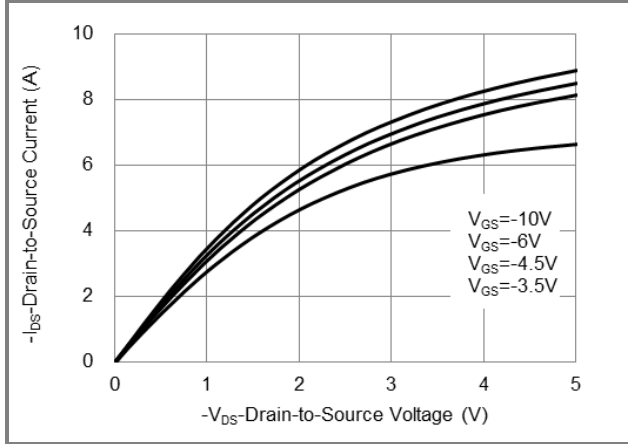
PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
<b>Static</b>						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V, I <sub>D</sub> =-250uA	-80	-	-	V
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =-250uA	-1.2	-1.4	-2.5	
Drain-Source On-State Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> =-10V, I <sub>D</sub> =-1.2A	-	286	360	mΩ
		V <sub>GS</sub> =-6V, I <sub>D</sub> =-1.1A	-	304	395	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =-80V, V <sub>GS</sub> =0V	-	-	-1	uA
Gate-Source Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V	-	-	±100	nA
<b>Dynamic</b> (Note 5)						
Total Gate Charge	Q <sub>g</sub>	V <sub>DS</sub> =-40V, I <sub>D</sub> =-1.2A, V <sub>GS</sub> =-10V (Note 2)	-	8.4	12	nC
Gate-Source Charge	Q <sub>gs</sub>		-	1.45	-	
Gate-Drain Charge	Q <sub>gd</sub>		-	1.2	-	
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> =-40V, V <sub>GS</sub> =0V, f=1MHz	-	421	650	pF
Output Capacitance	C <sub>oss</sub>		-	20	35	
Reverse Transfer Capacitance	C <sub>rss</sub>		-	12	21	
Gate resistance	R <sub>g</sub>	f=1MHz	-	11.3	-	Ω
Turn-On Delay Time	t <sub>d(on)</sub>	V <sub>DS</sub> =-40V, I <sub>D</sub> =-1.2A, V <sub>GS</sub> =-10V, R <sub>G</sub> =6Ω (Note 1,2)	-	3.6	-	ns
Turn-On Rise Time	t <sub>r</sub>		-	3	-	
Turn-Off Delay Time	t <sub>d(off)</sub>		-	22	-	
Turn-Off Fall Time	t <sub>f</sub>		-	9	-	
<b>Drain-Source Diode</b>						
Diode Forward Current	I <sub>S</sub>	T <sub>A</sub> =25°C	-	-	-1.4	A
Pulsed Diode Forward Current	I <sub>SM</sub>	(Package Limit)	-	-	-5.6	
Diode Forward Voltage	V <sub>SD</sub>	I <sub>S</sub> =-1A, V <sub>GS</sub> =0V	-	-0.8	-1.3	V
Reverse Recovery Time	T <sub>rr</sub>	V <sub>DD</sub> =-40V, V <sub>GS</sub> =0V,	-	15	-	ns
Reverse Recovery Charge	Q <sub>rr</sub>	I <sub>S</sub> =-1.2A, dI <sub>S</sub> /dt=100A/us	-	11	-	nC

NOTES :

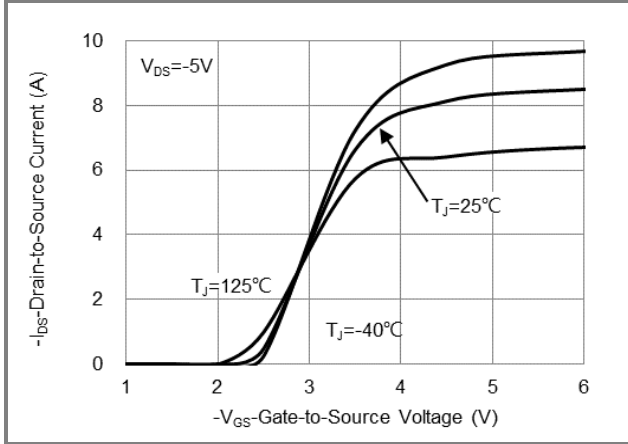
1. Pulse width ≤ 300us, Duty cycle ≤ 2%.
2. Essentially independent of operating temperature typical characteristics.
3. Chip capability with an R<sub>θJA</sub>=100°C/W.
4. R<sub>θJA</sub> is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. Mounted on a 1 inch<sup>2</sup> with 2oz. square pad of copper.
5. Guaranteed by design, not subject to production testing.

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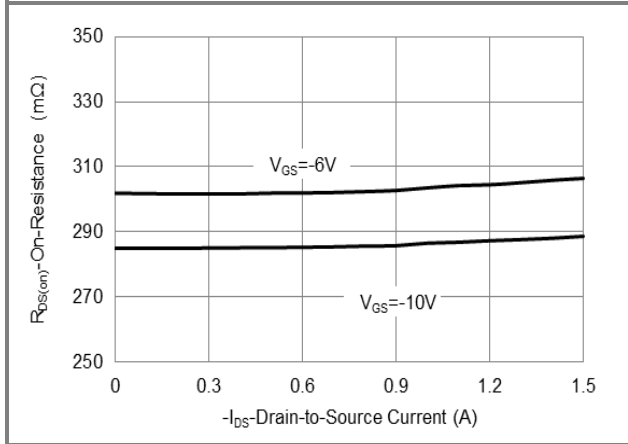
## TYPICAL CHARACTERISTIC CURVES



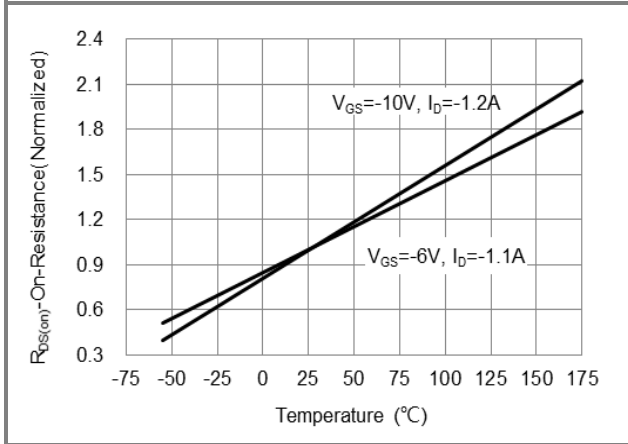
**Fig.1 On-Region Characteristics**



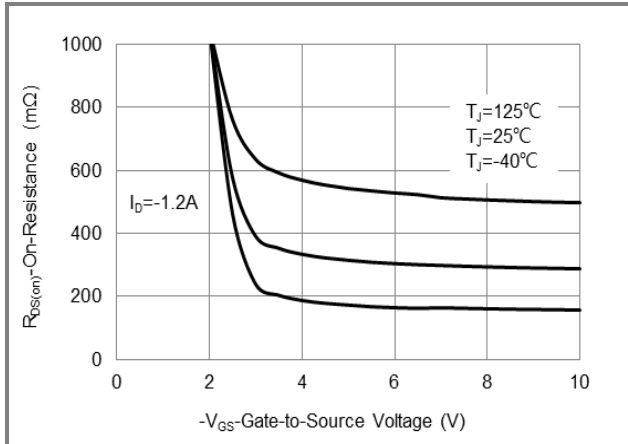
**Fig.2 Transfer Characteristics**



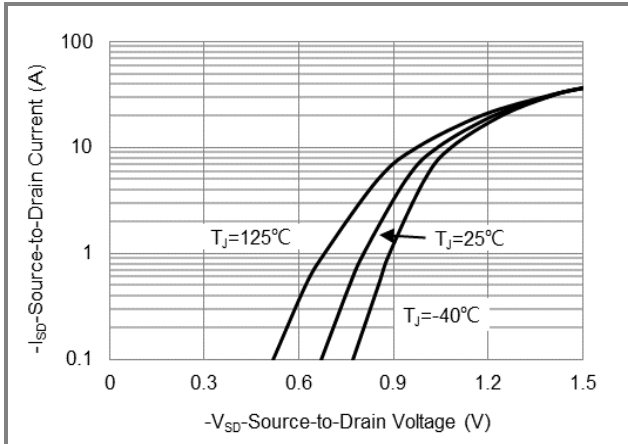
**Fig.3 On-Resistance vs. Drain Current**



**Fig.4 On-Resistance vs. Junction temperature**



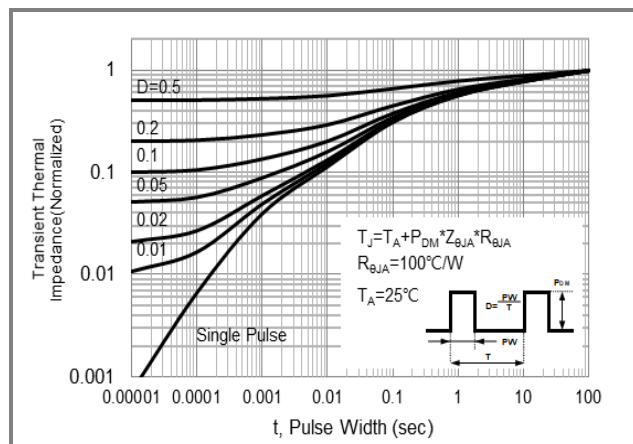
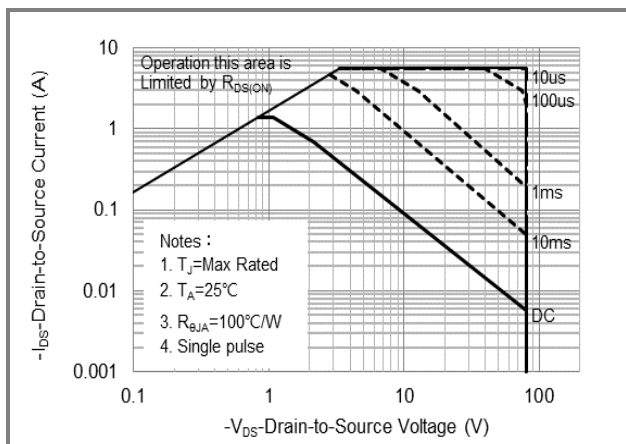
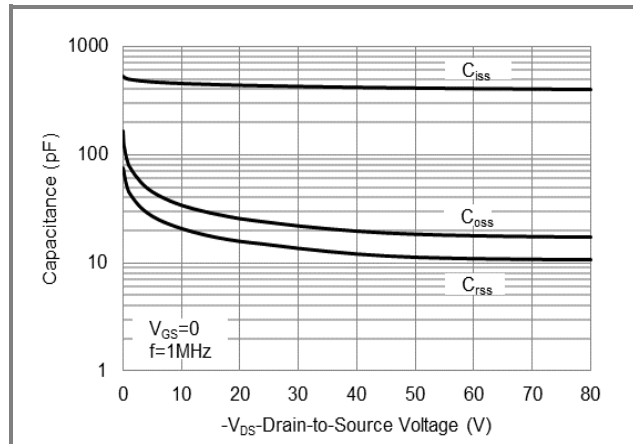
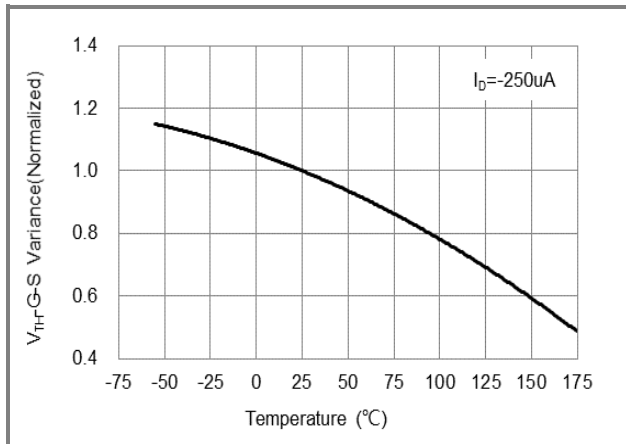
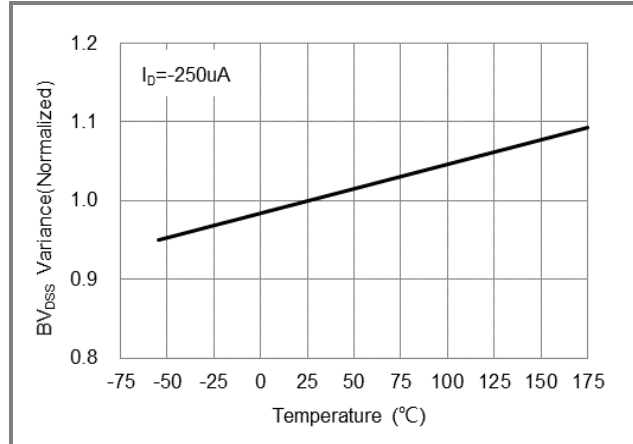
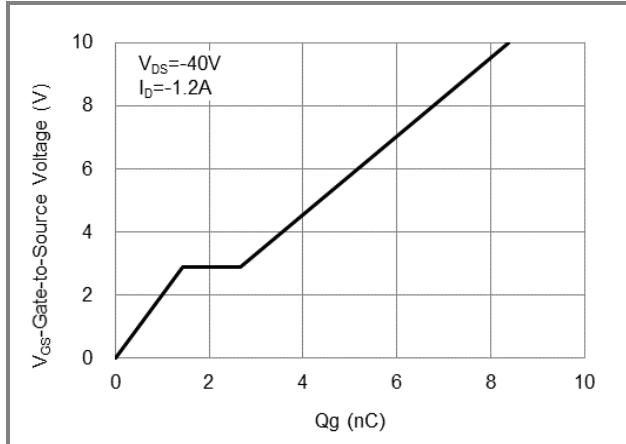
**Fig.5 On-Resistance Variation with V\_GS**



**Fig.6 Source-Drain Diode Forward Voltage**

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## TYPICAL CHARACTERISTIC CURVES

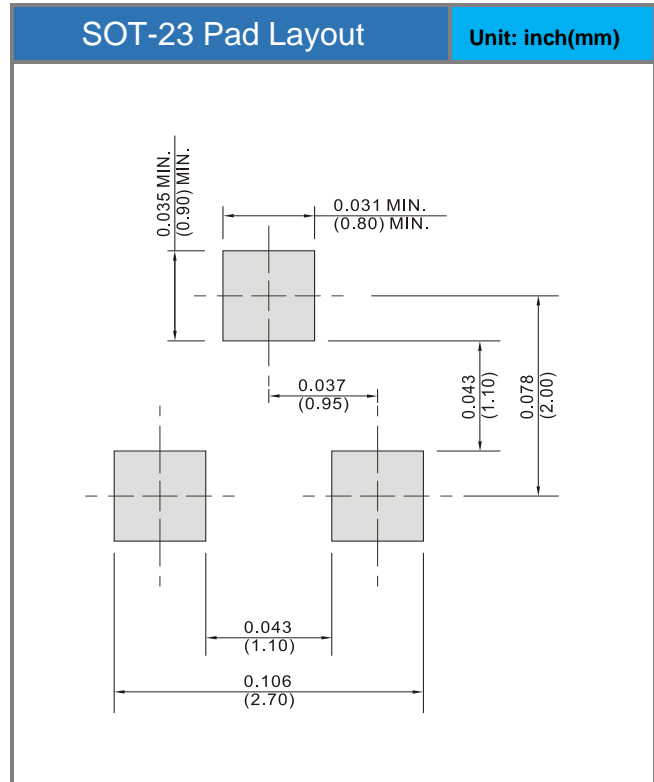
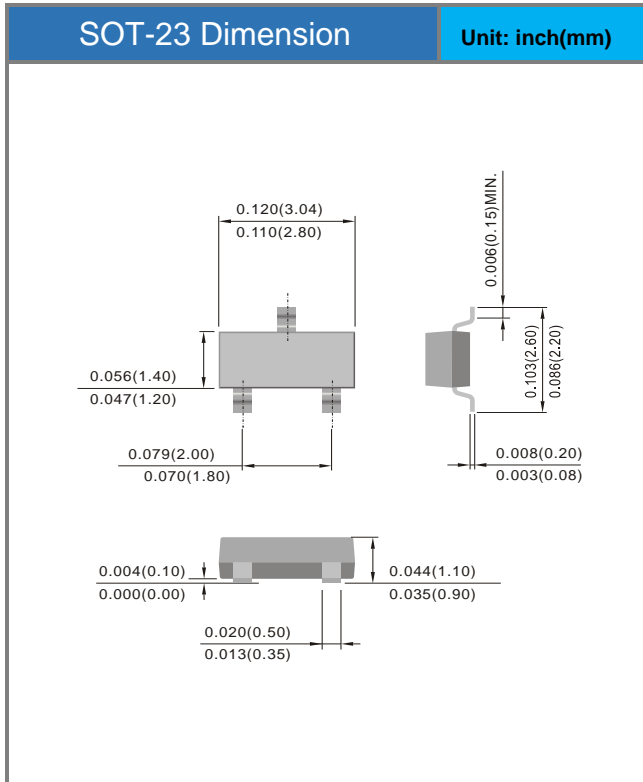


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## Product and Packing Information

Part No.	Package Type	Packing Type	Marking
PJA34817-AU	SOT-23	3K pcs / 7" reel	817

## Packaging Information & Mounting Pad Layout



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