

PJA3460S-AU

60V N-Channel Enhancement Mode MOSFET

Voltage	60 V	Current	2.5 A
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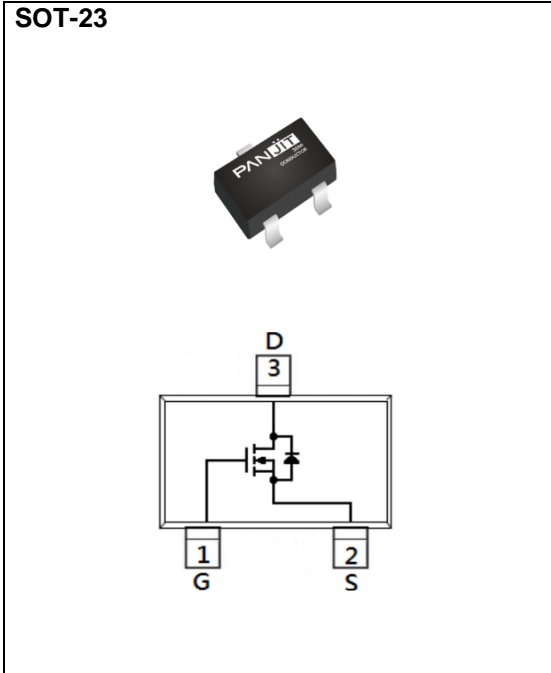
Features

- $R_{DS(ON)}$, $V_{GS}@10V$, $I_D@2A < 78m\Omega$
- $R_{DS(ON)}$, $V_{GS}@4.5V$, $I_D@1A < 98m\Omega$
- Advanced Trench Process Technology
- Logic Level Drive
- Specially Designed for Switch Load, PWM Application, etc
- AEC-Q101 qualified
- Lead free in compliance with EU RoHS 2.0
- Green molding compound as per IEC 61249 standard

Mechanical Data

- Case : SOT-23 Package
- Terminals : Solderable per MIL-STD-750, Method 2026
- Approx. Weight : 0.0084 grams

SOT-23



Maximum Ratings and Thermal Characteristics ($T_A=25^\circ\text{C}$ unless otherwise noted)

PARAMETER		SYMBOL	LIMIT	UNIT
Drain-Source Voltage		V_{DS}	60	V
Gate-Source Voltage		V_{GS}	± 20	
Continuous Drain Current ^(Note 5)	$T_A=25^\circ\text{C}$	I_D	2.5	A
	$T_A=70^\circ\text{C}$		2	
Pulsed Drain Current ^(Note 1)	$T_A=25^\circ\text{C}$	I_{DM}	10	
Power Dissipation	$T_A=25^\circ\text{C}$	P_D	1.25	W
	$T_A=70^\circ\text{C}$		0.8	
Operating Junction and Storage Temperature Range		T_J, T_{STG}	-55~150	$^\circ\text{C}$
Thermal Resistance		$R_{\theta JA}$	100	$^\circ\text{C/W}$
- Junction to Ambient ^(Note 4)				

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Electrical Characteristics (T_A=25°C unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Static						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V, I _D =250uA	60	-	-	V
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _D =250uA	1	1.8	2.5	
Drain-Source On-State Resistance	R _{DS(on)}	V _{GS} =10V, I _D =2A	-	62	78	mΩ
		V _{GS} =4.5V, I _D =1A	-	75	98	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =60V, V _{GS} =0V	-	-	1	uA
Gate-Source Leakage Current	I _{GSS}	V _{GS} =±20V, V _{DS} =0V	-	-	±100	nA
Dynamic (Note 6)						
Total Gate Charge	Q _g	V _{DS} =30V, I _D =2A, V _{GS} =10V (Note 2,3)	-	7.7	-	nC
Gate-Source Charge	Q _{gs}		-	1.4	-	
Gate-Drain Charge	Q _{gd}		-	1.3	-	
Input Capacitance	C _{iss}	V _{DS} =30V, V _{GS} =0V, f=1MHz	-	418	-	pF
Output Capacitance	C _{oss}		-	27	-	
Reverse Transfer Capacitance	C _{rss}		-	20	-	
Turn-On Delay Time	t _{d(on)}	V _{DD} =30V, I _D =2A, V _{GS} =10V, R _G =6Ω (Note 2,3)	-	5	-	ns
Turn-On Rise Time	t _r		-	22.4	-	
Turn-Off Delay Time	t _{d(off)}		-	21	-	
Turn-Off Fall Time	t _f		-	21.4	-	
Drain-Source Diode						
Maximum Continuous Drain-Source Diode Forward Current	I _S	T _A =25°C	-	-	2	A
Diode Forward Voltage	V _{SD}	I _S =1A, V _{GS} =0V	-	0.77	1.2	V

NOTES :

1. Pulse width < 300us, Duty cycle < 2%.
2. Essentially independent of operating temperature typical characteristics.
3. Repetitive rating, pulse width limited by junction temperature T_{J(MAX)}=150°C. Ratings are based on low frequency and duty cycles to keep initial T_J = 25°C.
4. R_{θJA} is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins mounted on a 1 inch FR-4 with 2oz. square pad of copper.
5. The maximum current rating is package limited.
6. Guaranteed by design, not subject to production testing.

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TYPICAL CHARACTERISTIC CURVES

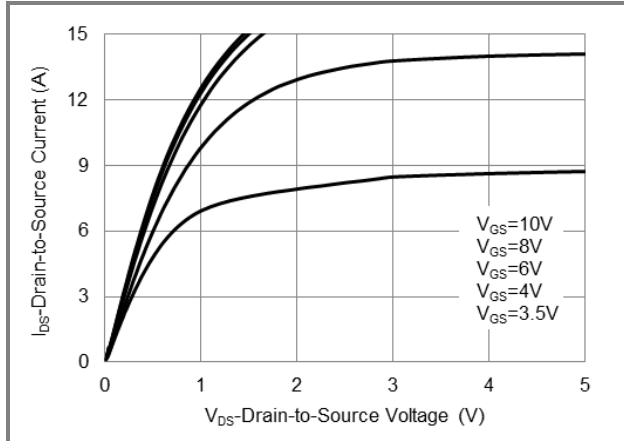


Fig.1 On-Region Characteristics

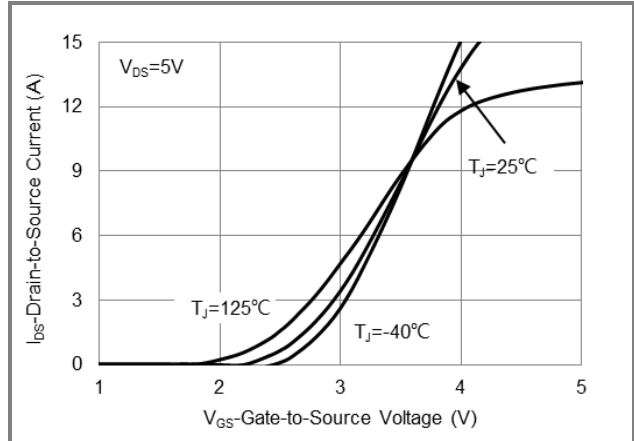


Fig.2 Transfer Characteristics

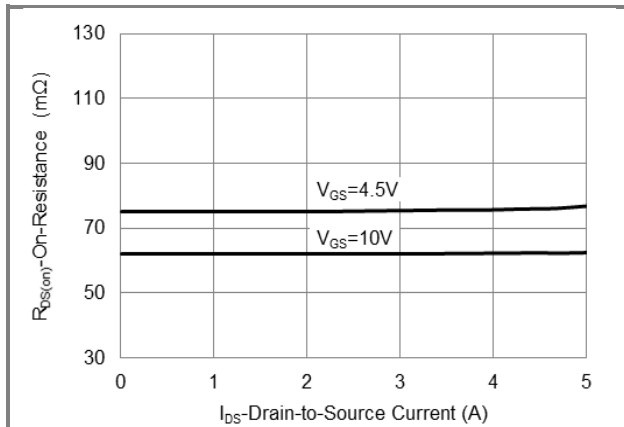


Fig.3 On-Resistance vs. Drain Current

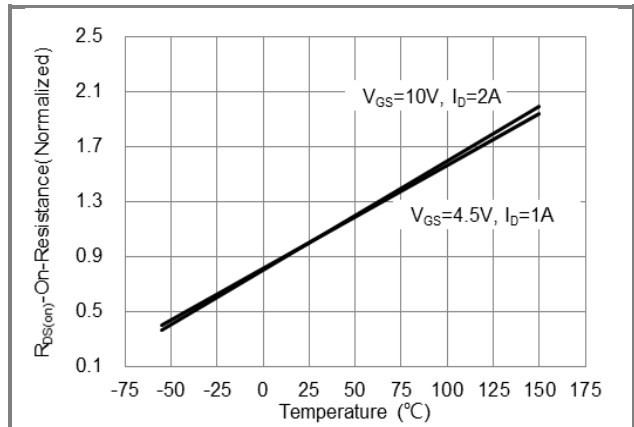


Fig.4 On-Resistance vs. Junction temperature

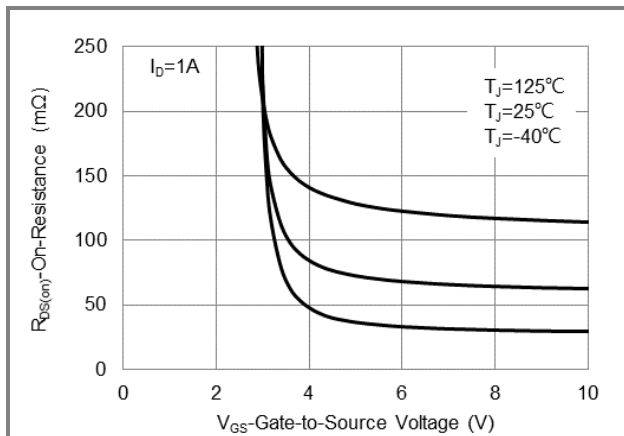


Fig.5 On-Resistance Variation with V_{GS}

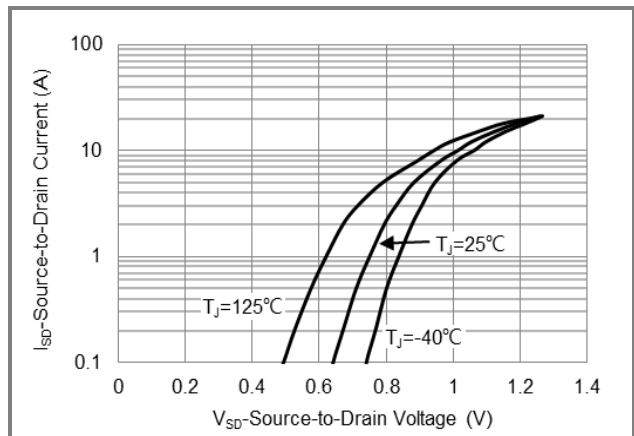


Fig.6 Source-Drain Diode Forward Voltage

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TYPICAL CHARACTERISTIC CURVES

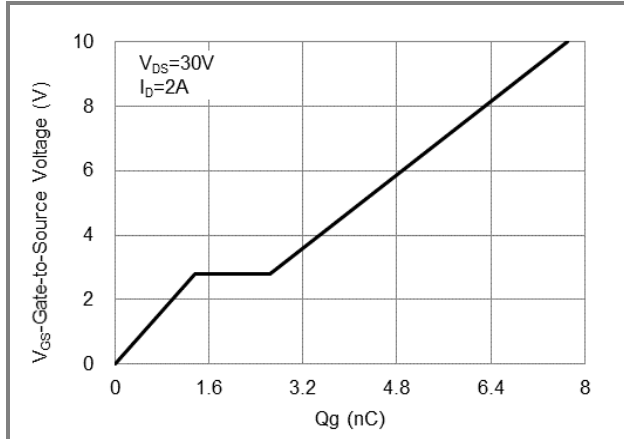


Fig.7 Gate-Charge Characteristics

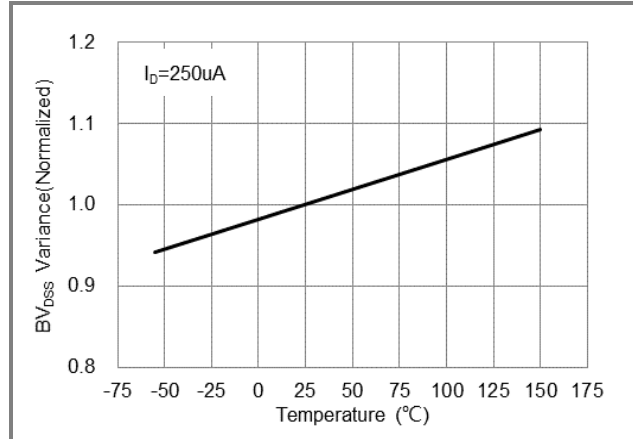


Fig.8 Breakdown Voltage Variation vs. Temperature

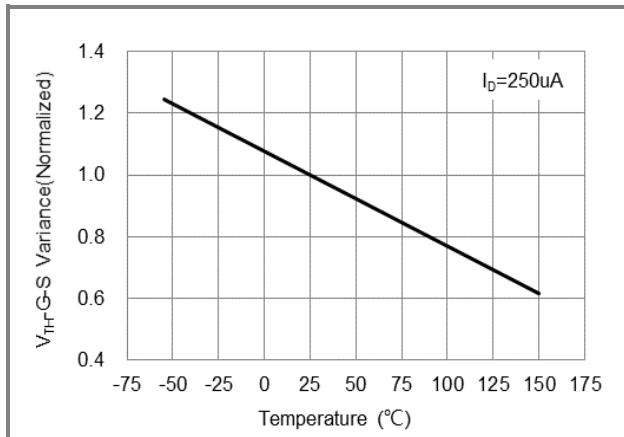


Fig.9 Threshold Voltage Variation with Temperature

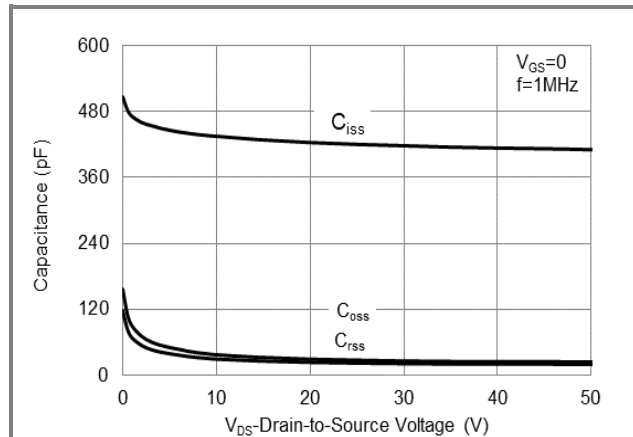


Fig.10 Capacitance vs. Drain-Source Voltage

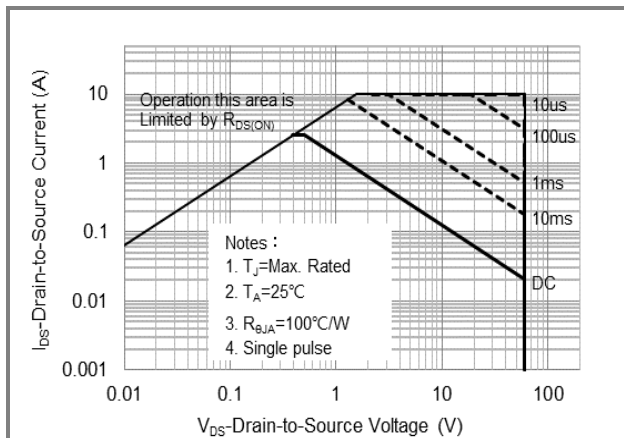


Fig.11 Maximum Safe Operating Area

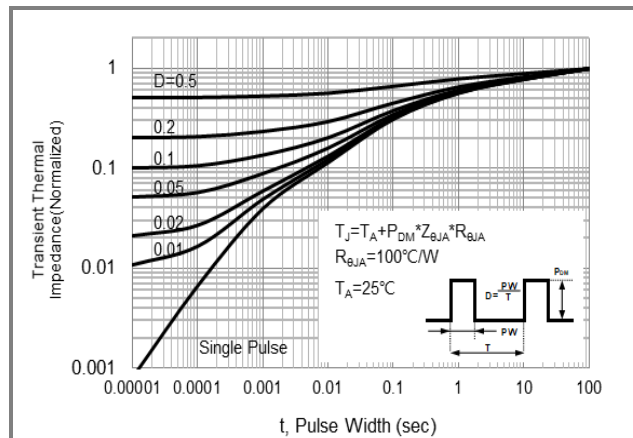


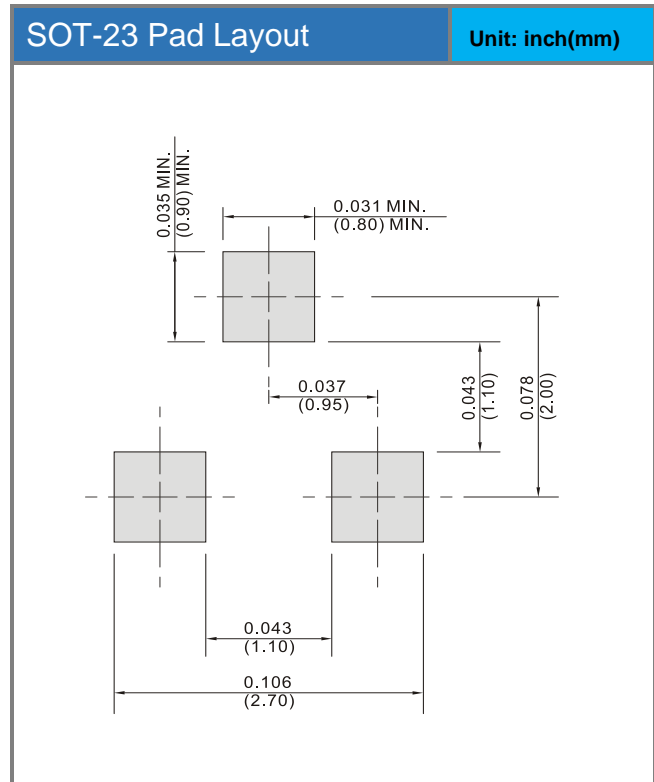
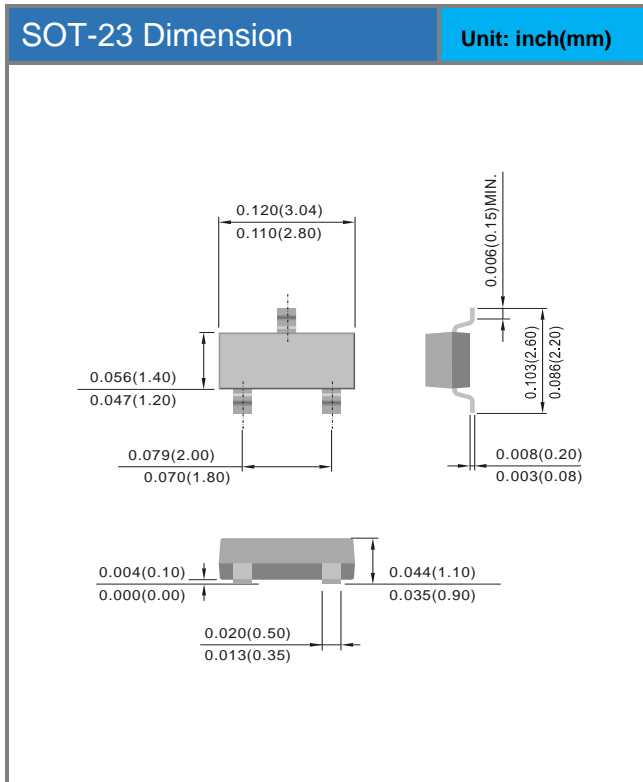
Fig.12 Normalized Transient Thermal Impedance

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Product and Packing Information

Part No.	Package Type	Packing Type	Marking
PJA3460S-AU	SOT-23	3K pcs / 7" reel	60S

Packaging Information & Mounting Pad Layout



PJA3460S-AU

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