

# PJQ2411B-AU

## 20V P-Channel Enhancement Mode MOSFET

**Voltage**    **-20 V**    **Current**    **-7 A**

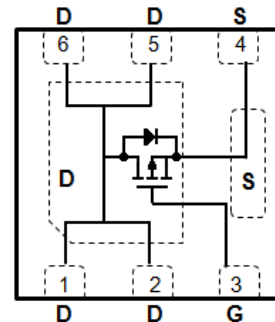
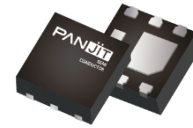
### Features

- $R_{DS(ON)}$ ,  $V_{GS}@-4.5V$ ,  $I_D@-3.1A < 110m\Omega$
- $R_{DS(ON)}$ ,  $V_{GS}@-2.5V$ ,  $I_D@-2A < 140m\Omega$
- $R_{DS(ON)}$ ,  $V_{GS}@-1.8V$ ,  $I_D@-1.1A < 190m\Omega$
- AEC-Q101 qualified
- Lead free in compliance with EU RoHS 2.0
- Green molding compound as per IEC 61249 standard

### Mechanical Data

- Case : DFN2020B-6L Package
- Terminals : Solderable per MIL-STD-750, Method 2026
- Approx. Weight : 0.0086 grams

DFN2020B-6L



### Maximum Ratings and Thermal Characteristics ( $T_A=25^\circ C$ unless otherwise noted)

PARAMETER		SYMBOL	LIMIT	UNIT
Drain-Source Voltage		$V_{DS}$	-20	V
Gate-Source Voltage		$V_{GS}$	$\pm 12$	
Continuous Drain Current <sup>(Note 3)</sup>	$T_C=25^\circ C$	$I_D$	-7	A
	$T_C=100^\circ C$		-4.5	
Pulsed Drain Current <sup>(Note 1)</sup>	$T_C=25^\circ C$	$I_{DM}$	-14	
Power Dissipation	$T_C=25^\circ C$	$P_D$	8.3	W
	$T_C=100^\circ C$		3.3	
Continuous Drain Current <sup>(Note 4)</sup>	$T_A=25^\circ C$	$I_D$	-2.9	A
	$T_A=70^\circ C$		-2.3	
Power Dissipation	$T_A=25^\circ C$	$P_D$	1.4	W
	$T_A=70^\circ C$		0.9	
Operating Junction and Storage Temperature Range		$T_J, T_{STG}$	-55~150	$^\circ C$
Thermal Resistance <sup>(Note 4)</sup>	Junction to Case	$R_{\theta JC}$	15	$^\circ C/W$
	Junction to Ambient	$R_{\theta JA}$	89	

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## Electrical Characteristics (T<sub>A</sub>=25°C unless otherwise noted)

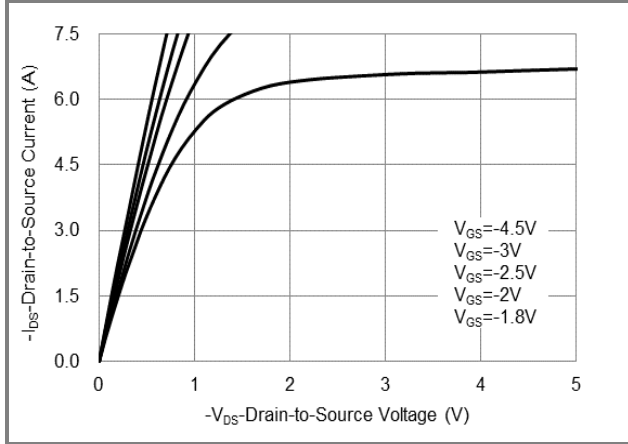
PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
<b>Static</b>						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V, I <sub>D</sub> =250uA	-20	-	-	V
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250uA	-0.4	-0.68	-1.2	
Drain-Source On-State Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-3.1A	-	85	110	mΩ
		V <sub>GS</sub> =-2.5V, I <sub>D</sub> =-2A	-	105	140	
		V <sub>GS</sub> =-1.8V, I <sub>D</sub> =-1.1A	-	130	190	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =-20V, V <sub>GS</sub> =0V	-	-	-1	uA
Gate-Source Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±12V, V <sub>DS</sub> =0V	-	-	±100	nA
<b>Dynamic</b> <sup>(Note 6)</sup>						
Total Gate Charge	Q <sub>g</sub>	V <sub>DS</sub> =-10V, I <sub>D</sub> =-3.1A, V <sub>GS</sub> =-4.5V <sup>(Note 2)</sup>	-	12	25	nC
Gate-Source Charge	Q <sub>gs</sub>		-	2	-	
Gate-Drain Charge	Q <sub>gd</sub>		-	2.4	-	
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> =-10V, V <sub>GS</sub> =0V, f=1MHz	-	427	650	pF
Output Capacitance	C <sub>oss</sub>		-	40	80	
Reverse Transfer Capacitance	C <sub>rss</sub>		-	32	65	
Turn-On Delay Time	t <sub>d(on)</sub>	V <sub>DS</sub> =-10V, I <sub>D</sub> =-3.1A, V <sub>GS</sub> =-4.5V, R <sub>G</sub> =6Ω (Note 2)	-	3.7	-	ns
Turn-On Rise Time	t <sub>r</sub>		-	3.5	-	
Turn-Off Delay Time	t <sub>d(off)</sub>		-	72	-	
Turn-Off Fall Time	t <sub>f</sub>		-	40	-	
<b>Drain-Source Diode</b>						
Diode Forward Current	I <sub>S</sub>	T <sub>C</sub> =25°C	-	-	-7	A
Pulsed Diode Forward Current	I <sub>SM</sub>	(Package Limit)	-	-	-14	
Diode Forward Voltage	V <sub>SD</sub>	I <sub>S</sub> =-1A, V <sub>GS</sub> =0V	-	-0.8	-1	V

**NOTES :**

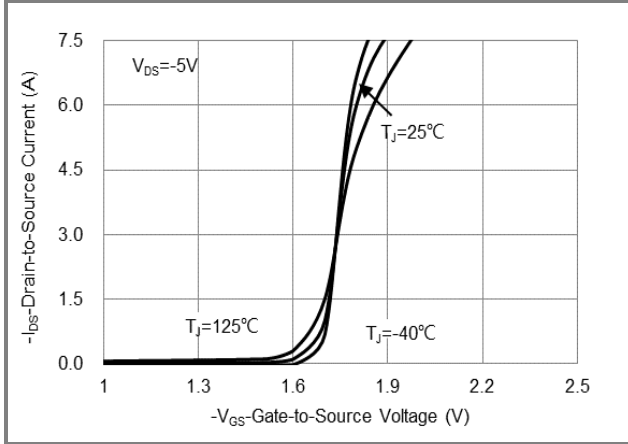
1. Pulse width ≤ 300us, Duty cycle ≤ 2%.
2. Essentially independent of operating temperature typical characteristics.
3. Repetitive rating, pulse width limited by junction temperature T<sub>J(MAX)</sub>=150°C. Ratings are based on low frequency and duty cycles to keep initial T<sub>J</sub>=25°C.
4. Chip capability with an R<sub>θJC</sub>=15°C/W.
5. R<sub>θJA</sub> is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. Mounted on a 1 inch<sup>2</sup> with 2oz.square pad of copper.
6. Guaranteed by design, not subject to production testing.

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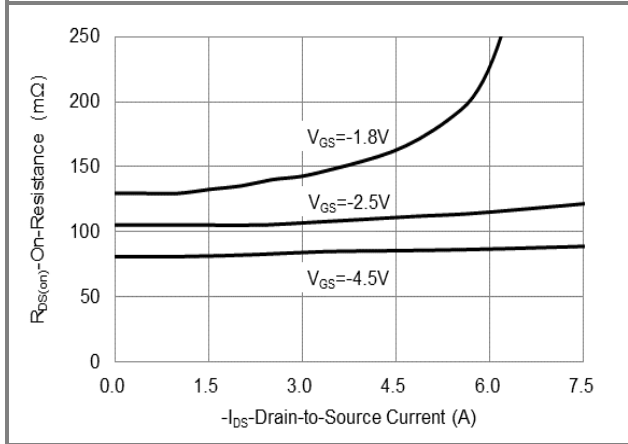
## TYPICAL CHARACTERISTIC CURVES



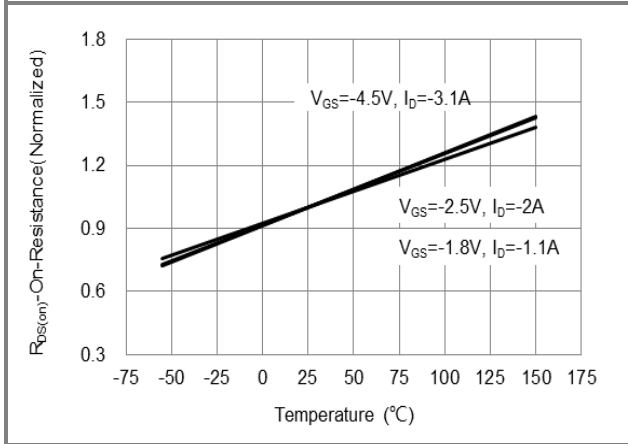
**Fig.1 On-Region Characteristics**



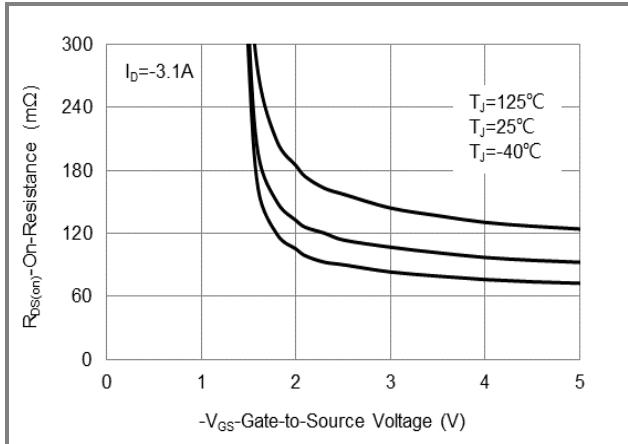
**Fig.2 Transfer Characteristics**



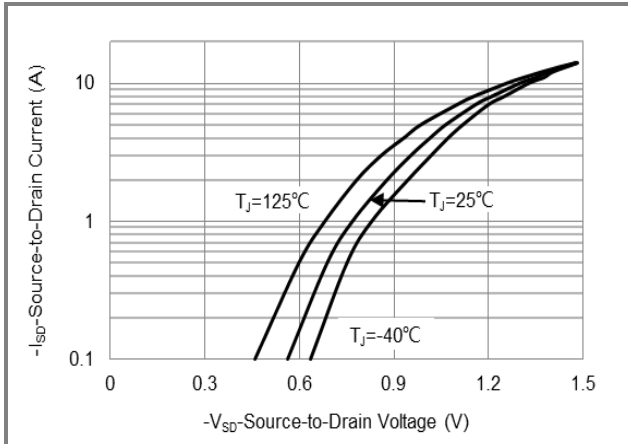
**Fig.3 On-Resistance vs. Drain Current**



**Fig.4 On-Resistance vs. Junction temperature**



**Fig.5 On-Resistance Variation with V\_GS**



**Fig.6 Source-Drain Diode Forward Voltage**

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## TYPICAL CHARACTERISTIC CURVES

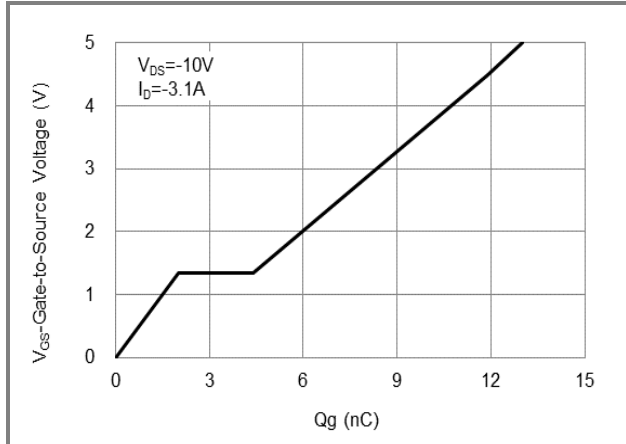


Fig.7 Gate-Charge Characteristics

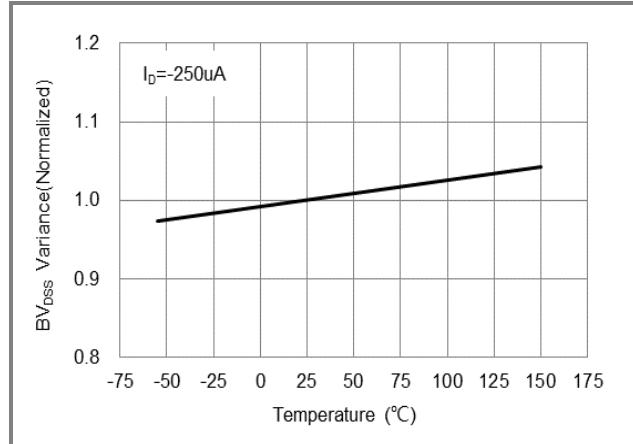


Fig.8 Breakdown Voltage Variation vs. Temperature

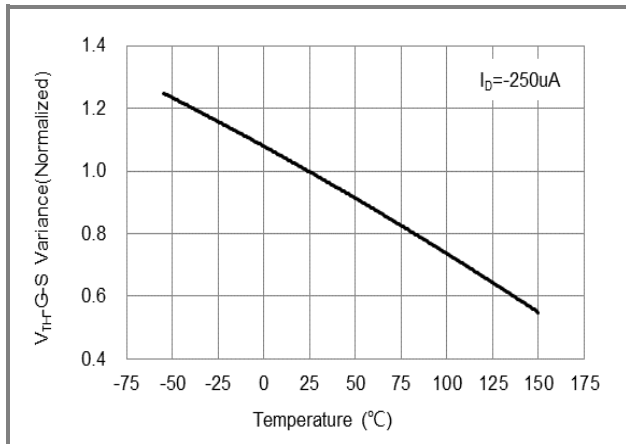


Fig.9 Threshold Voltage Variation with Temperature

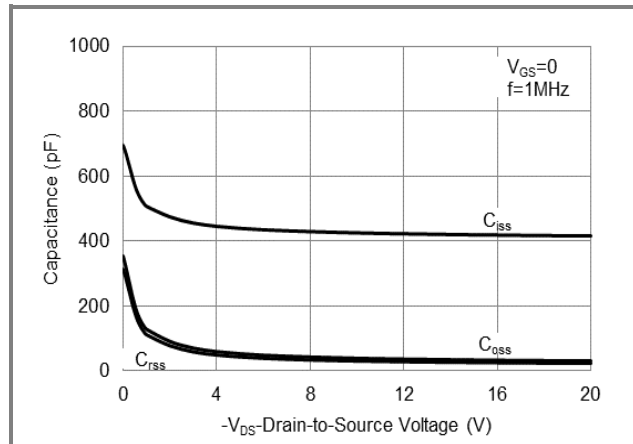


Fig.10 Capacitance vs. Drain-Source Voltage

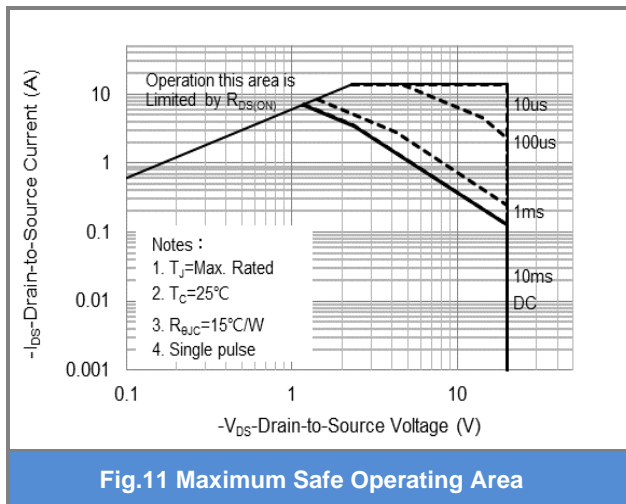


Fig.11 Maximum Safe Operating Area

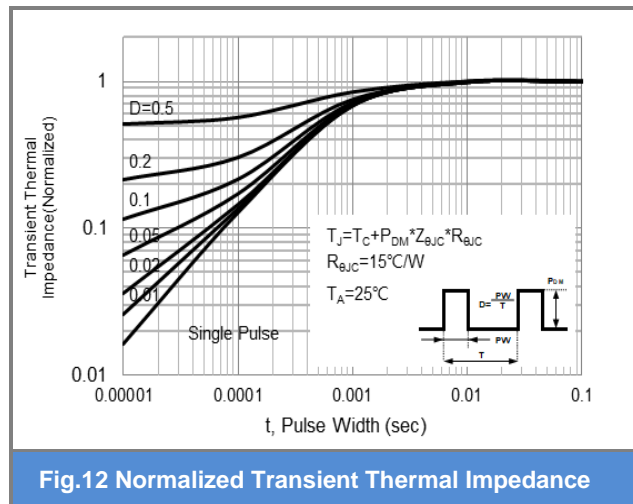


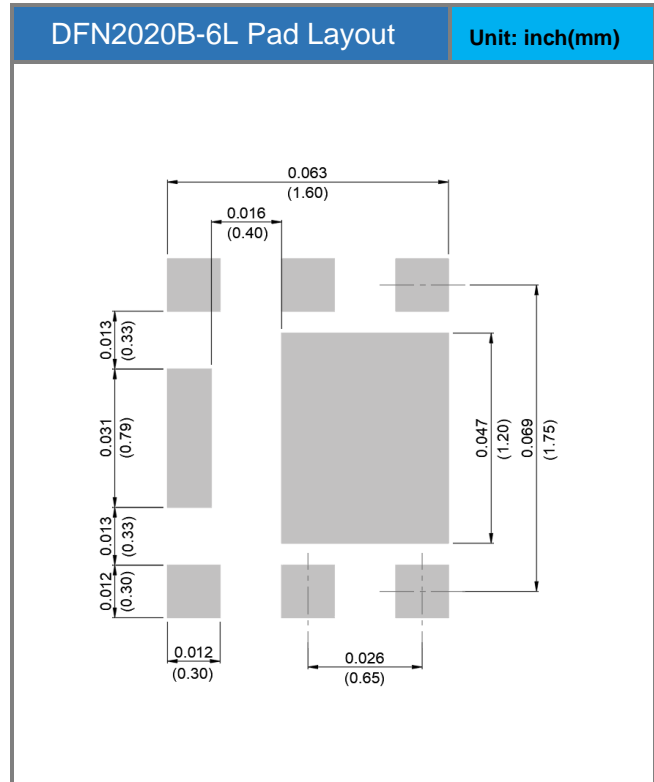
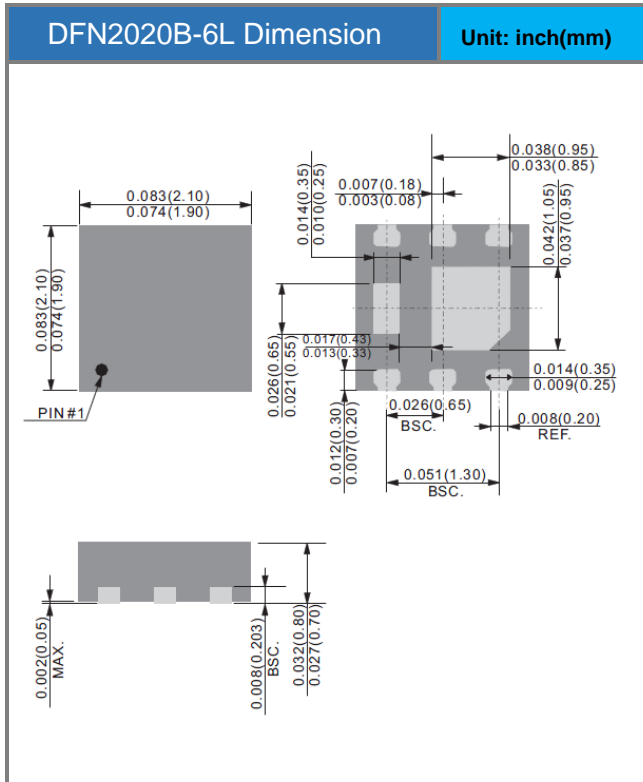
Fig.12 Normalized Transient Thermal Impedance

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## Product and Packing Information

Part No.	Package Type	Packing Type	Marking
PJQ2411B-AU	DFN2020B-6L	3K pcs / 7" reel	41B

## Packaging Information & Mounting Pad Layout



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