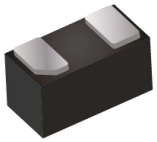
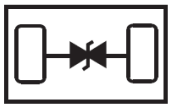
PJSD12LCFN2-AU

Bi-Directional ESD Protection Diode

Voltage **12 V**

DFN1006-2L





Features

- 115W Power Dissipation (8/20us Waveform)
- Low Leakage Current, Maximum of 0.1uA@12Vdc
- Very Low Clamping Voltage
- IEC 61000-4-2(ESD)：±30kV Air, ±30kV Contact Compliance
- AEC-Q101 qualified
- Lead free in compliance with EU RoHS 2.0
- Green molding compound as per IEC 61249 standard

Mechanical Data

- Case : DFN1006-2L Package
- Terminals : Solderable per MIL-STD-750, Method 2026
- Approx. Weight : 0.0006 grams

Maximum Ratings and Thermal Characteristics (T_A = 25°C unless otherwise noted)

PARAMETER	SYMBOL	LIMIT	UNIT
Peak Pulse Power (8/20us Waveform)	P_{PP}	115	W
Peak Pulse Power (8/20us Waveform)	I_{PPM}	5	A
ESD Voltage Per IEC61000-4-2 (Contact)	V_{ESD}	±30	kV
ESD Voltage Per IEC61000-4-2 (Air)		±30	
ESD Voltage (HBM)		±8	
Operating Junction Temperature Range	T_J	-55~150	°C
Storage Temperature Range	T_{STG}	-55~150	°C

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Electrical Characteristics (T_A = 25°C unless otherwise noted)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Reverse Stand-Off Voltage	V_{RWM}	-	-	-	12	V
Reverse Breakdown Voltage	V_{BR}	I_{BR} = 1 mA	13	-	-	V
Reverse Leakage Current	I_R	V_R = 12 V	-	-	0.1	uA
Clamping Voltage	V_C	I_{PP} = 1 A, t_P = 8/20us	-	-	20	V
		I_{PP} = 5 A, t_P = 8/20 us	-	-	23	V
Off State Junction Capacitance	C_J	0 Vdc Bias f = 1 MHz	-	15	-	pF

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TYPICAL CHARACTERISTIC CURVES

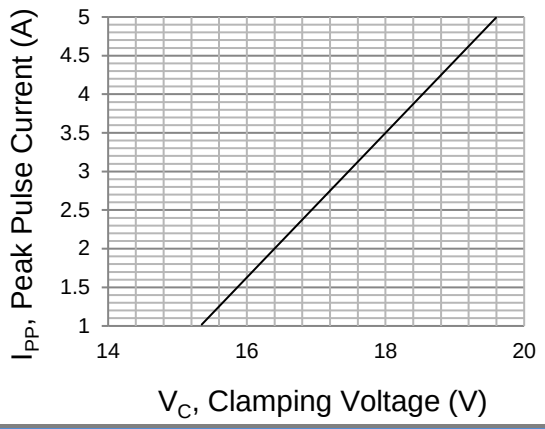

Fig.1 Typical peak clamping voltage(8/20us)

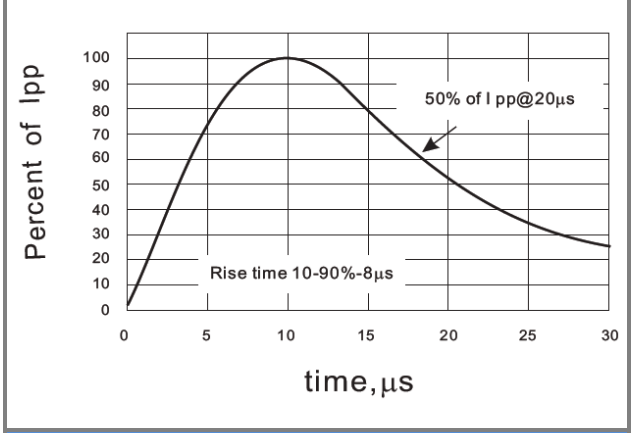

Fig.2 Pulse Waveform

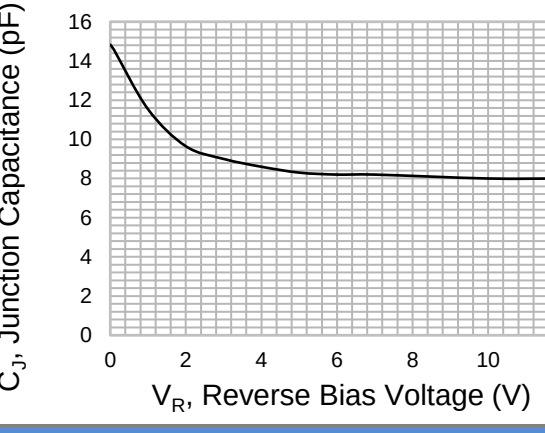

Fig.3 Typical Junction Capacitance

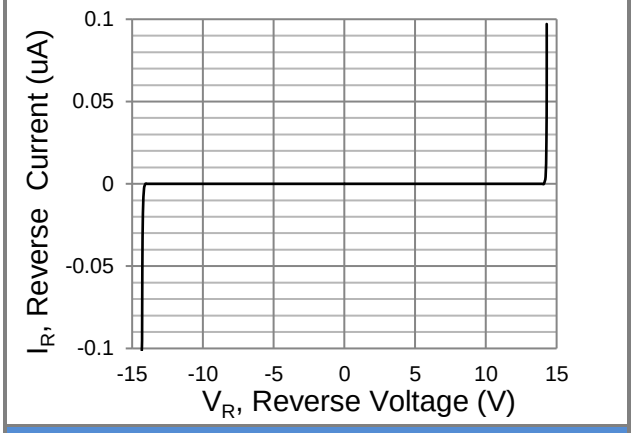

Fig.4 Typical Reverse Characteristics

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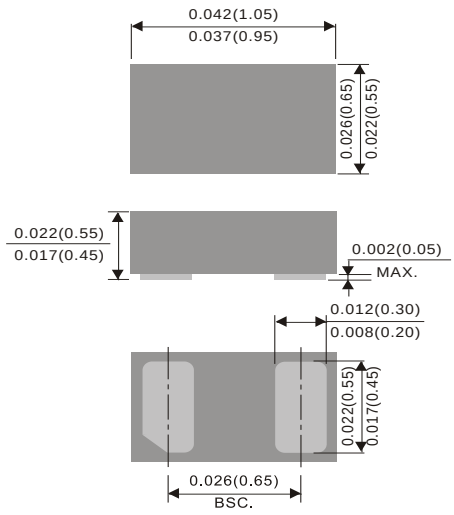
Product and Packing Information

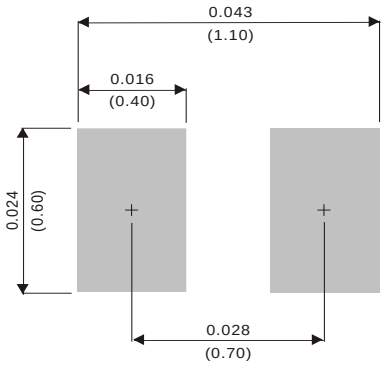
Part No.	Package Type	Packing Type	Marking
PJSD12LCFN2-AU	DFN1006-2L	10K pcs / 7" reel	BX

Packaging Information & Mounting Pad Layout

DFN1006-2L Dimension — Unit: inch(mm)



DFN1006-2L Pad Layout — Unit: inch(mm)



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