	nel Enhanc	ement Mode	MOSFE	T – ES	D Protected	
Voltage	30 V	Current	0.6A		SOT-523	
Features					0.043(1.1	
 RDS(ON) , VGS@4,5V, ID@0.6A<220mΩ 					0.067(1.70) 0.059(1.50) 0.044(1.10) 0.035(0.90)	
 RDS(ON) , VGS@2.5V, ID@0.4A<290mΩ 						
 RDS(ON), VGS@1.8V, ID@0.1A<600mΩ 						0.007(0.17) 0.002(0.07)
Advanced Trench Process Technology						
 Specially Designed for Switch Load, PWM Application, etc. 					0.067(1.7	
ESD Protected 2KV HBM						
Lead free in o	compliance with	EU RoHS 2.0				P
 Green molding compound as per IEC 61249 standard 						
					0.012(0.30) 0.004(0.10)	
Mechanica	I Data					D 3
Case : SOT-523 Package						
 Terminals : Solderable per MIL-STD-750, Method 2026 						⊣⊑≛
Approx. Weight : 0.002 grams						→/4 ↓
Marking : E0	4				1	2

Maximum Ratings and Thermal Characteristics (T_A=25°C unless otherwise noted)

PARAME	SYMBOL	LIMIT	UNITS	
Drain-Source Voltage	V _{DS}	30	V	
Gate-Source Voltage	V _{GS}	<u>+</u> 8	V	
Continuous Drain Current	lo	0.6	А	
Pulsed Drain Current	I _{DM}	2.4	А	
	Ta=25°C		300	mW
Power Dissipation	Derate above 25°C	PD	2.4	mW/ºC
Operating Junction and Storage	Temperature Range	TJ,TSTG -55~150		
Typical Thermal Resistance - Junction to Ambient ^(Note 3)	Reja	417	∘C/W	



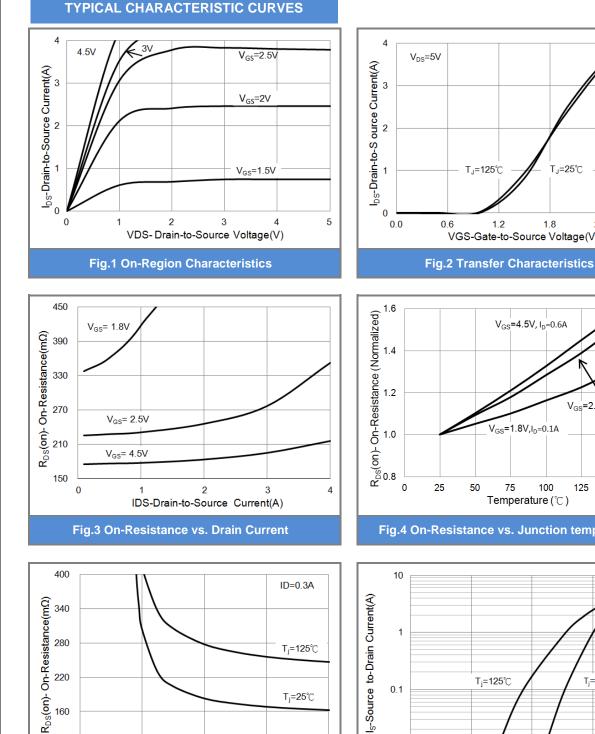
Electrical Characteristics (T_A=25°C unless otherwise noted)

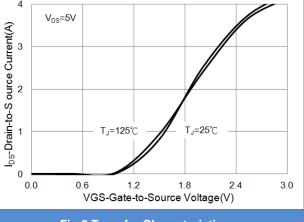
PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNITS
Static						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V, I _D =250uA	30	-	-	V
Gate Threshold Voltage	$V_{GS(th)}$	V _{DS} =V _{GS} , I _D =250uA	0.5	0.79	1.3	V
		V _{GS} =4.5V, I _D =0.6A	-	177	220	
Drain-Source On-State Resistance	$R_{\text{DS(on)}}$	V _{GS} =2.5V, I _D =0.4A	-	223	290	mΩ
		Vgs=1.8V, Id=0.1A	-	330	600	
Zero Gate Voltage Drain Current	IDSS	V _{DS} =30V, V _{GS} =0V	-	0.01	1	uA
Gate-Source Leakage Current	lgss	V _{GS=<u>+</u>8V, V_{DS}=0V}	-	<u>+</u> 1.5	<u>+</u> 10	uA
Dynamic ^(Note 5)						
Total Gate Charge	Qg		-	1.5	-	nC
Gate-Source Charge	Q_{gs}	V _{DS} =15V, I _D =0.6A, V _{GS} =4.5V ^(Note 1,2)	-	0.3	-	
Gate-Drain Charge	Q_{gd}	V _{GS} =4.5V ^(1000 1,2)	-	0.3	-	
Input Capacitance	Ciss		-	93	-	
Output Capacitance	Coss	V _{DS} =15V, V _{GS} =0V,	-	19	-	pF
Reverse Transfer Capacitance	Crss	f=1.0MHZ	-	6	-	
Turn-On Delay Time	td _(on)		-	6	-	ns
Turn-On Rise Time	tr	V _{DD} =15V, I _D =0.6A,	-	33	-	
Turn-Off Delay Time	td _(off)	$V_{GS}=4.5V$,	-	37	-	
Turn-Off Fall Time	tf	$R_G=6\Omega^{(Note 1,2)}$	-	32	-	
Drain-Source Diode						
Maximum Continuous Drain-Source Diode Forward Current	ls		-	-	0.4	А
Diode Forward Voltage	V _{SD}	Is=1A, V _{GS} =0V	-	0.81	1.2	V

NOTES :

- 1. Pulse width</br>
- 2. Essentially independent of operating temperature typical characteristics.
- 3. R_{0JA} is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins mounted on a 1 inch FR-4 with 2oz. square pad of copper
- 4. The maximum current rating is package limited
- 5. Guaranteed by design, not subject to production testing.







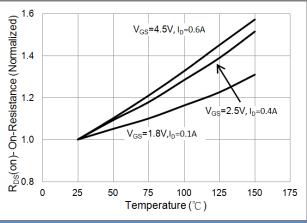
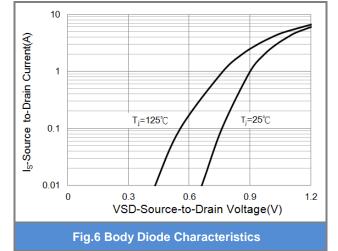


Fig.4 On-Resistance vs. Junction temperature



100

0

2

4

VGS-Gate-to-Source Voltage(V)

Fig.5 On-Resistance Variation with VGS.

6

8



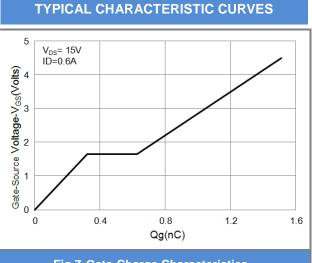


Fig.7 Gate-Charge Characteristics

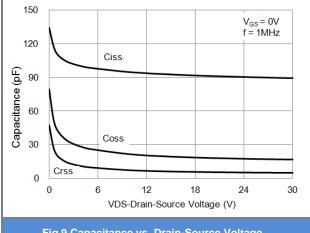


Fig.9 Capacitance vs. Drain-Source Voltage.

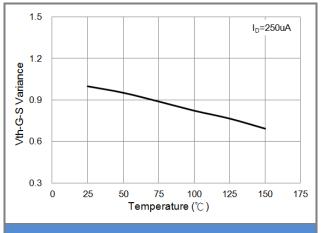


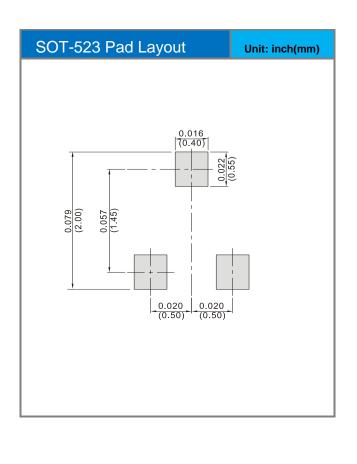
Fig.8 Threshold Voltage Variation with Temperature.



PART NO. PACKING CODE VERSION

Part No. Packing Code	Package Type	Packing Type	Marking	Version
PJE8404_R1_00001	SOT-523	4K pcs / 7" reel	E04	Halogen free RoHS compliant

MOUNTING PAD LAYOUT







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